Non-blocking synchronization for multi-core processors

The problem with the ever increasing power consumption and heat dissipation in modern processors has moved the issue of **parallelism** into the forefront.

Dual-core processors are now commonplace and Intel has already presented a research processor with 80 cores.

Programs now have to be specially designed to run faster on **multi-core** processors. They need to be divided into tasks that can be performed in parallel on different cores. For these tasks to be able to **communicate** with each other they have to have access to safe and efficient **synchronization**.

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**Locks**

A **lock** provides a very simple form of synchronization and is one of the most commonly used methods of synchronization. The lock gives one task an exclusive access to a certain memory area. This can lead to **poor parallelization** and introduces the risk of deadlocks if the code is not designed correctly. It also makes objects hard to compose.

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**STM**

**Software transactional memory** is a method that allows the programmer to mark sections of the code as atomic so that either all operations in the section are performed or none. This makes it **easy for programmers** to write concurrent but not necessarily efficient code.

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**Lock-free**

**Lock-free** data structures allow tasks to communicate with each other in a **highly efficient** manner. They provide objects that can be easily composed, however they can be somewhat complicated to design.

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**Splay tree**

*I'm currently trying to provide a parallel lock-free version of a splay tree.*

A splay tree is a commonly used data structure. It is a self-balancing tree that gives better performance than any other tree when faced with a non-uniform sequence of searches. It behaves like a cache and gives faster access to more recently used data.

**Challenges**

- Frequently used data is gathered at the root which increases conflicts.
- Every access to the tree changes it which is expensive.
- No extra data should be added to the tree.