

A Typed Logic for Stacks and Jumps

DRAFT

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Abstract

This note shows how one may define a program logic in the style of Floyd and Hoare for a simple, typed, stack-based, imperative language with unstructured control flow and local variables.

1 Introduction

Recent interest in security and certification has led to Floyd-Hoare and VDM-style programming logics becoming surprisingly fashionable, and to much work on type systems and specification logics for low-level code. Two industrially significant typed intermediate languages have received a great deal of attention: the bytecode of the JVM, used as a target for Java, and the Common Intermediate Language of the CLR, used as a target for languages including *C*[#] and Visual Basic. Both of these intermediate languages are stack-based¹ with control flow expressed using labelled jumps.

Most work on formalizing the type systems of these intermediate languages [10, 4] has treated the reality of stacks and jumps, though some authors have chosen to work with structured imperative control flow [5] or functional-style applicative expressions [11].

Even recent work on more general specification logics [1, 7, 6] has, however, mostly been done in the context of high-level languages. Borgström [2] has approached the problem of proving bytecode programs meet specifications by first decompiling them into higher-level structured code and then reasoning in standard Floyd-Hoare logic. Quigley [8, 9] has formalized rules for Hoare-like reasoning about a small subset of Java bytecode within Isabelle, but her treatment is based on trying to rediscover high-level control structures (such as while loops); this leads to rules which are both complex and rather weak.

Here we present and prove the correctness of a simple logic for directly proving partial correctness assertions on a minimal stack-based language with

¹Actually a rather poor design decision, but it's done now...

jumps. This is slightly more complex than traditional Hoare logic for while programs, as we have to deal with

- Unstructured control flow
- A stack which varies in size
- Globals and stack locations which vary in type
- Errors (underflow and type errors) arising from the previous factors

The first is not a significant problem (Floyd dealt with unstructured flowcharts in 1967 [3]). Here we address the other difficulties by imposing a simple type system to track the types of locations and the size of the stack and then defining assertions relative to those types.

2 The Language

2.1 Syntax

The syntax of basic values, instructions and programs is as follows. We assume the existence of a set \mathbb{V} of variables (this is naturally taken to be finite if one thinks of them as local variables for a method or registers).

$$\begin{aligned}
 n &\in \mathbb{Z} \\
 b &\in \mathbb{B} = \{true, false\} \\
 v &:= b \mid n \\
 l &\in \mathbb{N} \quad \text{labels} \\
 x &\in \mathbb{V} \quad \text{variables} \\
 op &\in \{+, *, -, <, >, =, \dots\} \\
 I &:= \text{pushc } v \mid \text{pushvar } x \mid \text{pop } x \mid \text{binop } op \mid \text{brtrue } l \mid \text{halt} \\
 p &:= [1 : I, 2 : I, \dots, l : I] \quad \text{programs}
 \end{aligned}$$

The `pushc` v instruction pushes a constant boolean or integer value v onto the stack. The `pushvar` x instruction pushes the value of the variable x onto the stack. The `pop` x instruction pops the top element off the stack and stores it in the variable x . The `binop` op instruction pops the top two elements off the stack and pushes the result of applying the binary operator op to them. The `brtrue` l instruction pops the top element of the stack and transfers control either to label l if the value was *true*, or to the next instruction if it was *false*. The `halt` instruction halts.

Note that programs can be viewed as functions from an initial segment of the strictly positive naturals to instructions. Thus we may use either functional or unordered list notation for them.

2.2 Operational Semantics

States are functions from variables to values (i.e. to the union of the booleans and the integers, which in this presentation we assume to be disjoint). Stacks are finite sequences of values:

$$S \ni States = \mathbb{V} \rightarrow (\mathbb{Z} \cup \mathbb{B})$$

$$\sigma \ni Stacks = (\mathbb{Z} \cup \mathbb{B})^*$$

A configuration consists of a program p and triple of a state, a stack and program counter (the current label). We present the operational semantics using a small-step transition relation:

$$[p]; \langle S, \sigma, l \rangle \rightarrow \langle S', \sigma', l' \rangle$$

defined by

$$\begin{aligned} & [p, l : \text{pushc } v]; \langle S, \sigma, l \rangle \rightarrow \langle S, (\sigma, v), l + 1 \rangle \\ & [p, l : \text{pushvar } x]; \langle S, \sigma, l \rangle \rightarrow \langle S, (\sigma, S(x)), l + 1 \rangle \\ & [p, l : \text{pop } x]; \langle S, (\sigma, v), l \rangle \rightarrow \langle S[v/x], \sigma, l + 1 \rangle \\ & [p, l : \text{binop } op]; \langle S, (\sigma, v_1, v_2), l \rangle \rightarrow \langle S, (\sigma, v_1 op v_2), l + 1 \rangle \quad \text{if } op \text{ defd} \\ & [p, l : \text{brtrue } l']; \langle S, (\sigma, \text{true}), l \rangle \rightarrow \langle S, \sigma, l' \rangle \\ & [p, l : \text{brfalse } l']; \langle S, (\sigma, \text{false}), l \rangle \rightarrow \langle S, \sigma, l + 1 \rangle \end{aligned}$$

and we define the multistep transition relation $[p]; \langle S, \sigma, l \rangle \rightarrow^* \langle S', \sigma', l' \rangle$ in the obvious way:

$$[p]; \langle S, \sigma, l \rangle \rightarrow^* \langle S, \sigma, l \rangle$$

$$\frac{[p]; \langle S, \sigma, l \rangle \rightarrow \langle S', \sigma', l' \rangle \quad [p]; \langle S', \sigma', l' \rangle \rightarrow^* \langle S'', \sigma'', l'' \rangle}{[p]; \langle S, \sigma, l \rangle \rightarrow^* \langle S'', \sigma'', l'' \rangle}$$

We sometimes write $[p]; \langle S, \sigma, l \rangle \Downarrow \langle S', \sigma' \rangle$ to mean $[p]; \langle S, \sigma, l \rangle \rightarrow^* \langle S', \sigma', l' \rangle$ for some l' such that $p(l') = \text{halt}$.

The following is obvious, assuming each primitive operation is a function, since each configuration has at most one successor.

Lemma 1. *The operational semantics is deterministic.* □

It is also worth noting that execution preserves, and is independent of, extensions to the stack at the bottom:

Lemma 2. *If $[p]; \langle S, \sigma, l \rangle \rightarrow^* \langle S', \sigma', l' \rangle$ then for any σ'' , $[p]; \langle S, (\sigma'', \sigma), l \rangle \rightarrow^* \langle S', (\sigma'', \sigma'), l' \rangle$.* □

3 Types and Assertions

As well as terminating executions that hit a `halt` instruction, programs can exhibit four other kinds of behaviour:

1. Divergence
2. Type errors
3. Stack underflow
4. Wild jumps

In the small step semantics, divergence corresponds to an infinite sequence of transitions, whilst stack underflow, type errors and wild jumps show up as configurations in which the current instruction is not `halt`, yet no transition is possible (stuck configurations).

Our goal is to define a Floyd-Hoare style logic. Clearly we will have predicates P of some sort on stacks and states, but there are various possible semantics for judgements² $\{P\} p, l \{Q\}$:

1. Total correctness. If $\langle S, \sigma \rangle \in \llbracket P \rrbracket$ then $[p]; \langle S, \sigma, l \rangle \Downarrow \langle S', \sigma' \rangle$ with $\langle S', \sigma' \rangle \in \llbracket Q \rrbracket$.
2. Very partial correctness. If $\langle S, \sigma \rangle \in \llbracket P \rrbracket$ and $[p]; \langle S, \sigma, l \rangle \Downarrow \langle S', \sigma' \rangle$, then $\langle S', \sigma' \rangle \in \llbracket Q \rrbracket$. Such specifications are satisfied by erroneous as well as diverging programs.
3. Error-free partial correctness. If $\langle S, \sigma \rangle \in \llbracket P \rrbracket$ then EITHER $[p]; \langle S, \sigma, l \rangle \Downarrow \langle S', \sigma' \rangle$ with $\langle S', \sigma' \rangle \in \llbracket Q \rrbracket$, OR $[p]; \langle S, \sigma, l \rangle$ diverges.
4. Error-tracking partial correctness. We enrich the language of predicates to include explicit treatment of erroneous states (as in the work of Jacobs and Huisman [6]).
5. Underspecified partial correctness. We modify the semantics to be defined but non-deterministic in some error cases (e.g. simple type errors like adding a boolean to an integer, maybe reading below the bottom of the stack). Then do normal partial correctness wrt the modified semantics.
6. Default values. Modify the semantics to return default values for accesses below the bottom of the stack.
7. Move to a relational interpretation of predicates.
8. ...

²Actually, our judgements are not of quite this form.

There also seems to be some scope for varying exactly how predicates on stacks themselves should be formulated, to deal with, or avoid, references to inaccessible stack locations.

Here we choose to rule out erroneous behaviour by means of a simple type system, and to define our predicates relative to those types. Thus we will only be able to prove properties of programs which are typeable. This seems natural, but it is worth stressing that it is not the only reasonable way to proceed – although both the JVM and CLR have type systems and type checkers ('verifiers'), the CLR does give a semantics to unverifiable code; such code can be executed if it has been granted sufficient permissions. Our type-based approach prevents one from proving any properties at all of unverifiable code.

3.1 Basic Types

We start by defining types for values, states and stacks:

$$\begin{aligned}\tau &\in \{\text{int}, \text{bool}\} && \text{base types} \\ \Sigma &:= - \mid \Sigma, \tau && \text{stack types} \\ \Delta &\in \mathbb{V} \rightarrow \{\text{int}, \text{bool}\} && \text{state types}\end{aligned}$$

We define $\llbracket \text{int} \rrbracket = \mathbb{Z}$ and $\llbracket \text{bool} \rrbracket = \mathbb{B}$. Typing for states is straightforward:

$$\vdash S : \Delta \text{ iff } \forall x \in \mathbb{V}. S(x) \in \llbracket \Delta(x) \rrbracket$$

For stacks, we use the following³ rules:

$$\vdash \sigma : - \quad \frac{\vdash \sigma : \Sigma \quad \vdash v : \tau}{\vdash \sigma, v : \Sigma, \tau}$$

We define substitution on states and state types like this:

$$\begin{aligned}(\Delta[x \mapsto \tau])(y) &= \begin{cases} \tau & \text{if } x = y \\ \Delta(y) & \text{otherwise} \end{cases} \\ (S[x \mapsto v])(y) &= \begin{cases} v & \text{if } x = y \\ S(y) & \text{otherwise} \end{cases}\end{aligned}$$

Lemma 3. *If $\vdash S : \Delta$ and $v : \tau$ then $\vdash S[x \mapsto v] : \Delta[x \mapsto \tau]$.*

□

Similarly for stacks, though here substitution is partial:

$$\begin{aligned}(\Sigma, \tau')[0 \mapsto \tau] &= \Sigma, \tau \\ (\Sigma, \tau')[i + 1 \mapsto \tau] &= \Sigma[i \mapsto \tau], \tau'\end{aligned}$$

$$\begin{aligned}(\sigma, v')[0 \mapsto v] &= \sigma, v \\ (\sigma, v')[i + 1 \mapsto v] &= \sigma[i \mapsto v], v'\end{aligned}$$

Lemma 4. *If $\vdash \sigma : \Sigma$ and $v : \tau$ then $\vdash \sigma[i \mapsto v] : \Sigma[i \mapsto \tau]$.*

□

³Note that these rules interpret stack types as constraints on the top of the stack, not as an exact specification of the whole stack.

3.2 Assertions and Program Types

To write specifications, we will need a syntax for boolean- and integer-valued expressions. This is given by the following grammar:

$$E := \underline{n} \mid \underline{b} \mid x \mid s(i) \mid E \text{ op } E$$

As before, the metavariable op ranges over all our binary operators, including arithmetic and logical ones. n and b are integer and boolean constants, and x ranges over \mathbb{V} . We also have an expression form $s(i)$ for i a natural number, which represents the i th element down the stack. (We could equally well use list notation, with $hd(s)$ and $tl(s)$, instead of indexing stack locations by natural numbers.)

Expressions can have type either `int` or `bool` in the context of a given stack and store typing:

$$\begin{array}{c} \Delta, \Sigma \vdash n : \text{int} \quad \Delta, \Sigma \vdash b : \text{bool} \\ (\Delta, x : \tau), \Sigma \vdash x : \tau \quad \Delta, (\Sigma, \tau) \vdash s(0) : \tau \\ \frac{\Delta, \Sigma \vdash s(i) : \tau \quad \Delta, \Sigma \vdash E_1 : \tau_1 \quad \Delta, \Sigma \vdash E_2 : \tau_2 \quad op : \tau_1 \times \tau_2 \rightarrow \tau_3}{\Delta, (\Sigma, \tau') \vdash s(i + 1) : \tau} \quad \frac{\Delta, \Sigma \vdash E_1 : \tau_1 \quad \Delta, \Sigma \vdash E_2 : \tau_2 \quad op : \tau_1 \times \tau_2 \rightarrow \tau_3}{\Delta, \Sigma \vdash E_1 \text{ op } E_2 : \tau_3} \end{array}$$

We define substitution $E[E'/x]$ and $E[E'/s(i)]$ in the usual way.

Lemma 5.

1. If $S[x \mapsto \tau'], \sigma \vdash E : \tau$ and $S, \sigma \vdash E' : \tau'$ then $S, \sigma \vdash E[E'/x] : \tau$.
2. If $S, \sigma[i \mapsto \tau'] \vdash E : \tau$ and $S, \sigma \vdash E' : \tau'$ then $S, \sigma \vdash E[E'/s(i)] : \tau$.

□

If $\Delta, \Sigma \vdash E : \tau$, $S : \Delta$ and $\sigma : \Sigma$ then we define $\llbracket E \rrbracket(S, \sigma) \in \llbracket \tau \rrbracket$ inductively as follows: (and this *is* well-defined):

$$\begin{array}{lcl} \llbracket \underline{n} \rrbracket(S, \sigma) & = & n \\ \llbracket \underline{b} \rrbracket(S, \sigma) & = & b \\ \llbracket x \rrbracket(S, \sigma) & = & S(x) \\ \llbracket s(0) \rrbracket(S, (\sigma, v)) & = & v \\ \llbracket s(i + 1) \rrbracket(S, (\sigma, v)) & = & \llbracket s(i) \rrbracket(S, \sigma) \\ \llbracket E_1 \text{ op } E_2 \rrbracket(S, \sigma) & = & \llbracket E_1 \rrbracket(S, \sigma) \text{ op } \llbracket E_2 \rrbracket(S, \sigma) \end{array}$$

Lemma 6.

1. If $S[x \mapsto \tau'], \sigma \vdash E : \tau$ and $S, \sigma \vdash E' : \tau'$ then

$$\llbracket E[E'/x] \rrbracket(S, \sigma) = \llbracket E \rrbracket(S[x \mapsto \llbracket E' \rrbracket(S, \sigma)], \sigma).$$

2. If $S, \sigma[i \mapsto \tau'] \vdash E : \tau$ and $S, \sigma \vdash E' : \tau'$ then

$$\llbracket E[E'/s(i)] \rrbracket(S, \sigma) = \llbracket E \rrbracket(S, \sigma[i \mapsto \llbracket E' \rrbracket(S, \sigma)]).$$

□

If $\Delta, \Sigma \vdash E_i : \text{bool}$ for $i \in \{1, 2\}$, we write $\Sigma, \Delta \models E_1 \implies E_2$ to mean

$$\forall S. \forall \sigma. \text{if } \vdash S : \Delta \text{ and } \vdash \sigma : \Sigma \text{ then } \llbracket E_1 \rrbracket(S, \sigma) \implies \llbracket E_2 \rrbracket(S, \sigma)$$

If E is an expression, then we define $\text{shift}(E)$ by

$$\begin{aligned} \text{shift}(s(i)) &= s(i+1) \\ \text{shift}(E_1 \text{ op } E_2) &= \text{shift}(E_1) \text{ op } \text{shift}(E_2) \\ \text{shift}(E) &= E \text{ otherwise} \end{aligned}$$

Lemma 7.

1. If $\Delta, \Sigma \vdash E : \tau$ then for any τ' , $\Delta, (\Sigma, \tau') \vdash \text{shift}(E) : \tau$.

2. If $\Delta, \Sigma \vdash E : \tau$, $\vdash S : \Delta$ and $\sigma : \Sigma$ then for any v ,

$$\llbracket E \rrbracket(S, \sigma) = \llbracket \text{shift}(E) \rrbracket(S, (\sigma, v)).$$

□

One can present the type system for programs and then the assertion checking rules on typed programs, but here we choose to combine them into one system. A program type Γ thus maps labels to triples of state type, stack type and assertion:

$$\Gamma := [1 : \Delta_1, \Sigma_1, E_1; \dots; l : \Delta_l, \Sigma_l, E_l]$$

with the well-typedness condition

$$\frac{\forall 1 \leq l' \leq l. \Delta_{l'}, \Sigma_{l'} \vdash E_{l'} : \text{bool}}{\vdash [1 : \Delta_1, \Sigma_1, E_1; \dots; l : \Delta_l, \Sigma_l, E_l]}$$

The rules for checking a particular instruction in a program in the context of a program typing Γ , assuming that $\vdash \Gamma$ holds, are shown in Figure 1. We then say a program is well-typed and specified, and write $\Gamma \vdash p$, if all its instructions are:

$$\frac{\vdash \Gamma \quad \forall 1 \leq l' \leq l. \Gamma \vdash I_{l'}}{\Gamma \vdash [1 : I_1, \dots, l : I_l]}$$

$$\begin{array}{c}
\Gamma \vdash l : \text{halt} \\
\\
\frac{\Gamma(l) = \Delta_l, \Sigma_l, E_l \quad \vdash v : \tau \quad \Gamma(l+1) = \Delta_l, (\Sigma_l, \tau), E_{l+1} \\
\Delta_l, (\Sigma_l, \tau) \models \text{shift}(E_l) \implies E_{l+1}[v/s(0)]}{\Gamma \vdash l : \text{pushc } v} \\
\\
\frac{\Gamma(l) = \Delta_l, \Sigma_l, E_l \quad \Delta_l(x) = \tau \quad \Gamma(l+1) = \Delta_l, (\Sigma_l, \tau), E_{l+1} \\
\Delta_l, (\Sigma_l, \tau) \models \text{shift}(E_l) \implies E_{l+1}[x/s(0)]}{\Gamma \vdash l : \text{pushvar } x} \\
\\
\frac{\Gamma(l) = \Delta_l, (\Sigma_{l+1}, \tau), E_l \quad \Gamma(l+1) = \Delta_l[x \mapsto \tau], \Sigma_{l+1}, E_{l+1} \\
\Delta_l, (\Sigma_{l+1}, \tau) \vdash E_l \implies (\text{shift}(E_{l+1}))[s(0)/x]}{\Gamma \vdash l : \text{pop } x} \\
\\
\frac{\Gamma(l) = \Delta_l, (\Sigma, \tau_1, \tau_2), E_l \quad \Gamma(l+1) = \Delta_l, (\Sigma, \tau_3), E_{l+1} \quad op : \tau_1 \times \tau_2 \rightarrow \tau_3 \\
\Delta_l, (\Sigma, \tau_2, \tau_1) \models E_l \implies (\text{shift}(E_{l+1}))[s(0) \text{ op } s(1)/s(1)]}{\Gamma \vdash l : \text{binop}_{op}} \\
\\
\frac{\Gamma(l) = \Delta_l, (\Sigma_{l+1}, \text{bool}), E_l \quad \Gamma(l+1) = \Delta_l, \Sigma_{l+1}, E_{l+1} \quad \Gamma(l') = \Delta_l, \Sigma_{l+1}, E_{l'} \\
\Delta_l, (\Sigma_{l+1}, \text{bool}) \models E_l \wedge \neg s(0) \implies \text{shift}(E_{l+1}) \\
\Delta_l, (\Sigma_{l+1}, \text{bool}) \models E_l \wedge s(0) \implies \text{shift}(E_{l'})}{\Gamma \vdash l : \text{brtrue } l'}
\end{array}$$

Figure 1: Rules for checking types and specifications

3.3 Soundness

We say what it means for a configuration to be well typed and specified with respect to an extended program type:

$$\frac{\Gamma \vdash p \quad \Gamma(l) = \Delta_l, \Sigma_l, E_l \quad \vdash \sigma : \Sigma_l \quad \vdash S : \Delta_l \quad \llbracket E_l \rrbracket(S, \sigma) = \text{true}}{\Gamma \vdash [p]; \langle S, \sigma, l \rangle}$$

Taking the requirements in order, we need that the whole program is well typed and specified (which includes the requirement that the extended program type is well-formed), the state and stack have the type assigned to them at the current label and meet the specification associated with this label.

Soundness of the logic with respect to the operational semantics is then formulated in the usual preservation and progress style:

Theorem 1. *If $\Gamma \vdash [p]; \langle S, \sigma, l \rangle$ then either $p(l) = \text{halt}$ or there exist S', σ', l' such that $[p]; \langle S, \sigma, l \rangle \rightarrow \langle S', \sigma', l' \rangle$ and $\Gamma \vdash [p]; \langle S', \sigma', l' \rangle$.*

Proof. We consider each case for the instruction I_l in turn.

halt Nothing to prove.

pushc v If we take $S' = S$, $\sigma' = (\sigma, v)$, $l' = l + 1$ then we have a transition. We know $\vdash S : \Delta_l$, $\vdash \sigma : \Sigma_l$ and $\llbracket E_l \rrbracket(S, \sigma) = \text{true}$ by assumption. We know $\Delta_{l'} = \Delta_l$, $\Sigma_{l'} = (\Sigma_l, \tau)$ and $v : \tau$ from the fact that I_l is well typed and specified. Hence we can deduce $\vdash \sigma' : \Sigma_{l'}$ and $\vdash S' : \Delta_{l'}$. Now

$$\begin{aligned} & \llbracket E_l \rrbracket(S, \sigma) = \text{true} \\ \implies & \llbracket \text{shift}(E_l) \rrbracket(S, (\sigma, v)) = \text{true} \quad \text{Lemma 7} \\ \implies & \llbracket E_{l'}[v/s(0)] \rrbracket(S, (\sigma, v)) = \text{true} \quad \text{assmp} \\ \implies & \llbracket E_{l'} \rrbracket(S', \sigma') = \text{true} \quad \text{Lemma 6} \end{aligned}$$

pushvar x Let $v = S(x)$ and take $S' = S$, $\sigma' = (\sigma, v)$, $l' = l + 1$. Then we have a transition. We know $\Delta_{l'} = \Delta_l$, $\Sigma_{l'} = \Sigma_l, \tau$, where $\tau = \Delta_l(x)$ by the type rules, so $\vdash S' : \Delta_{l'}$ and $\vdash \sigma' : \Sigma_{l'}$. For the specification, we then reason essentially as in the case above.

pop x Take $l' = l + 1$ then we know there's a τ such that $\Sigma_l = (\Sigma_{l'}, \tau)$, $\Delta_{l'} = \Delta_l[x \mapsto \tau]$ from typing, and $\vdash S : \Delta_l$ and $\vdash \sigma : \Sigma_l$ by assumption. So $\sigma = (\sigma', v)$ for some $\sigma' : \Sigma_{l'}$ and $v : \tau$. Let $S' = S[x \mapsto v]$ so we have a transition, and $S' : \Delta_{l'}$ by Lemma *.

$$\begin{aligned} & \llbracket E_l \rrbracket(S, \sigma) = \text{true} \\ \implies & \llbracket \text{shift}(E_{l'})[s(0)/x] \rrbracket(S, \sigma) = \text{true} \quad \text{assmp} \\ \implies & \llbracket \text{shift}(E_{l'})[s(0)/x] \rrbracket(S, (\sigma', v)) = \text{true} \\ \implies & \llbracket \text{shift}(E_{l'}) \rrbracket(S[x \mapsto \llbracket s(0) \rrbracket(S, (\sigma', v))], (\sigma', v)) = \text{true} \quad \text{Lemma 6} \\ \implies & \llbracket \text{shift}(E_{l'}) \rrbracket(S[x \mapsto v], (\sigma', v)) = \text{true} \\ \implies & \llbracket E_{l'} \rrbracket(S', \sigma') = \text{true} \end{aligned}$$

as required.

binop_{op} Take $l' = l + 1$ then we know that $\Sigma_l = (\Sigma, \tau_2, \tau_1)$ and $\Sigma_{l'} = (\Sigma, \tau_3)$ for some Σ , where $op : \tau_1 \times \tau_2 \rightarrow \tau_3$. Then as $\vdash \sigma : \Sigma_l$ we must have $\sigma = (\sigma'', v_2, v_1)$ for some $\vdash \sigma'' : \Sigma$, $v_2 : \tau_2$ and $v_1 : \tau_1$. Let $\sigma' = (\sigma'', v_1 op v_2)$ and then $\vdash \sigma' : \Sigma_{l'}$. For the state, we know $\vdash S : \Delta_l$ and $\Delta_{l'} = \Delta_l$ so we take $S' = S$ and we have typing and a transition. Now

$$\begin{aligned} & \llbracket E_l \rrbracket(S, \sigma) = \text{true} \\ \implies & \llbracket E_l \rrbracket(S, (\sigma'', v_2, v_1)) = \text{true} \\ \implies & \llbracket \text{shift}(E_{l'})[(s(0) op s(1))/s(1)] \rrbracket(S, (\sigma'', v_2, v_1)) = \text{true} \\ \implies & \llbracket \text{shift}(E_{l'}) \rrbracket(S, (\sigma'', \llbracket s(0) op s(1) \rrbracket(S, (\sigma'', v_2, v_1)), v_1)) = \text{true} \\ \implies & \llbracket \text{shift}(E_{l'}) \rrbracket(S, (\sigma'', v_1 op v_2, v_1)) = \text{true} \\ \implies & \llbracket E_{l'} \rrbracket(S, (\sigma'', v_1 op v_2)) = \text{true} \\ \implies & \llbracket E_{l'} \rrbracket(S, \sigma') = \text{true} \end{aligned}$$

as required.

brtrue k We know $\Delta_{l+1} = \Delta_k = \Delta_l$, and $\Sigma_{l+1} = \Sigma_k$ and $\Sigma_l = (\Sigma_k, \text{bool})$. So $\sigma = (\sigma', b)$ for some boolean b , and $\vdash \sigma' : \Sigma_k$ by typing. If $b = \text{true}$ then

we have a transition with $l' = k$ and $S' = S$, so $\vdash S' : \Delta_{l'}$ is immediate. Then for the spec

$$\begin{aligned}
 & \llbracket E_l \rrbracket(S, \sigma) = \text{true} \\
 \implies & \llbracket E_l \rrbracket(S, (\sigma', \text{true})) = \text{true} \\
 \implies & \llbracket E_l \wedge s(0) \rrbracket(S, (\sigma', \text{true})) = \text{true} \\
 \implies & \llbracket \text{shift}(E_k) \rrbracket(S, (\sigma', \text{true})) = \text{true} \\
 \implies & \llbracket E_k \rrbracket(S, \sigma') = \text{true} \\
 \implies & \llbracket E_{l'} \rrbracket(S', \sigma') = \text{true}
 \end{aligned}$$

as required. The case for $b = \text{false}$ and $l' = l + 1$ is similar. \square

3.4 Examples

Consider the source program

```

x := 0;
while(5>x) {
    x := x+1;
}

```

A typical Java or C^\sharp compiler would produce code roughly like the following, where we have annotated each instruction with a type and specification. Note that the while loop is compiled with the test and conditional backwards branch at the end, and is preceded by a header which branches unconditionally into the loop to execute the test the first time.

l	I_l	$\Delta_l; \Sigma_l$	E_l
1	pushc 0	$x : \text{int}; -$	true
2	pop x	$x : \text{int}; \text{int}$	$s(0) = 0$
3	pushc true	$x : \text{int}; -$	$x = 0$
4	brtrue 9	$x : \text{int}; \text{bool}$	$x = 0 \wedge s(0) = \text{true}$
5	pushvar x	$x : \text{int}; -$	$x < 5$
6	pushc 1	$x : \text{int}; \text{int}$	$s(0) < 5$
7	binop _{add}	$x : \text{int}; \text{int}, \text{int}$	$s(1) < 5 \wedge s(0) = 1$
8	pop x	$x : \text{int}; \text{int}$	$s(0) \leq 5$
9	pushvar x	$x : \text{int}; -$	$x \leq 5$
10	pushc 5	$x : \text{int}; \text{int}$	$x \leq 5 \wedge s(0) = x$
11	binop _{gt}	$x : \text{int}; \text{int}, \text{int}$	$x \leq 5 \wedge s(1) = x \wedge s(0) = 5$
12	brtrue 5	$x : \text{int}; \text{bool}$	$x \leq 5 \wedge s(0) = (x < 5)$
13	halt	$x : \text{int}; -$	$x = 5$

Here are the justifications that each instruction is well-typed and specified according to the rules:

1. $x : \text{int}, (\text{int}) \models \text{shift}(\text{true}) \implies (s(0) = 0)[0/s(0)]$ which is valid.

2. $x : \text{int}, (\text{int}) \models s(0) = 0 \implies \text{shift}(x = 0)[s(0)/x]$ which is valid.
3. $x : \text{int}, (\text{bool}) \models \text{shift}(x = 0) \implies (x = 0 \wedge s(0) = \text{true})[\text{true}/s(0)]$ which is valid.
4. Here there are two things to check. $x : \text{int}, (\text{bool}) \models x = 0 \wedge s(0) = \text{true} \wedge \neg s(0) \implies \text{shift}(x < 5)$ which holds because we have false on the left, and $x : \text{int}, (\text{bool}) \models x = 0 \wedge s(0) = \text{true} \wedge s(0) \implies \text{shift}(x \leq 5)$ which is valid.
5. $x : \text{int}, (\text{int}) \models \text{shift}(x < 5) \implies (s(0) < 5)[x/s(0)]$ which is valid.
6. $x : \text{int}, (\text{int}, \text{int}) \models \text{shift}(s(0) < 5) \implies (s(1) < 5 \wedge s(0) = 1)[1/s(0)]$ which is $x : \text{int}, (\text{int}, \text{int}) \models s(1) < 5 \implies (s(1) < 5 \wedge 1 = 1)$ which is valid.
7. $x : \text{int}, (\text{int}, \text{int}) \models s(1) < 5 \wedge s(0) = 1 \implies \text{shift}(s(0) \leq 5)[(s(0) + s(1))/s(1)]$ which is $x : \text{int}, (\text{int}, \text{int}) \models s(1) < 5 \wedge s(0) = 1 \implies s(0) + s(1) \leq 5$ which is valid.
8. $x : \text{int}, (\text{int}) \models s(0) \leq 5 \implies \text{shift}(x \leq 5)[s(0)/x]$ which is valid.
9. $x : \text{int}, (\text{int}) \models \text{shift}(x \leq 5) \implies (x \leq 5 \wedge s(0) = x)[x/s(0)]$ which is valid.
10. $x : \text{int}, (\text{int}, \text{int}) \models \text{shift}(x \leq 5 \wedge s(0) = x) \implies (x \leq 5 \wedge s(1) = x \wedge s(0) = 5)[5/s(0)]$ which is $x : \text{int}, (\text{int}, \text{int}) \models x \leq 5 \wedge s(1) = x \implies (x \leq 5 \wedge s(1) = x \wedge 5 = 5)$ which is valid.
11. $x : \text{int}, (\text{int}, \text{int}) \models (x \leq 5 \wedge s(1) = x \wedge s(0) = 5) \implies \text{shift}(x \leq 5 \wedge s(0) = (x < 5))[(s(0) > s(1))/s(1)]$ which is $x : \text{int}, (\text{int}, \text{int}) \models (x \leq 5 \wedge s(1) = x \wedge s(0) = 5) \implies \text{shift}(x \leq 5 \wedge (s(0) > s(1)) = (x < 5))$ which is valid.
12. Two things to prove: $x : \text{int}, (\text{bool}) \models x \leq 5 \wedge s(0) = (x < 5) \wedge \neg s(0) \implies \text{shift}(x = 5)$ which is valid, and $x : \text{int}, (\text{bool}) \models x \leq 5 \wedge s(0) = (x < 5) \wedge s(0) \implies \text{shift}(x < 5)$ which is valid.
13. Nothing to prove.

Thus we have established that the program is well-typed and that the value of x will be 5 on termination.

4 Conclusions

We have presented a typed program logic for a tiny stack-based intermediate language (bearing roughly the same relation to Java bytecode or CIL that the

language of while programs does to Java or C^\sharp). The logic is proved to be sound with respect to an operational semantics.

Nothing about the logic is amazingly novel, though the interplay between the type system and assertion language (the size of the stack and the types of stack locations and locals can vary) and the use of the $shift(\cdot)$ operation are interesting. As far as I can ascertain, this simple and natural system has not been presented before, though it certainly seems worth writing down.

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