A Single Intermediate Language That Supports Multiple Implementations of Exceptions

Norman Ramsey
Harvard University
nr@eecs.harvard.edu

Simon Peyton Jones
Microsoft Research Ltd
simonpj@microsoft.com

ABSTRACT
We present mechanisms that enable our compiler-target language, C++, to express four of the best known techniques for implementing exceptions, all within a single, uniform framework. We define the mechanisms precisely, using a formal operational semantics. We also show that exceptions need not require special treatment in the optimizer; by introducing extra dataflow edges, we make standard optimization techniques work even on programs that use exceptions. Our approach clarifies the design space of exception-handling techniques, and it allows a single optimizer to handle a variety of implementations. Our ultimate goal is to allow a source-language compiler the freedom to choose its exception-handling policy, while encapsulating the architecture-dependent mechanisms and their optimization in an implementation of C++ that can be used by compilers for many source languages.

1. INTRODUCTION
C++ is a compiler-target language intended to be independent of both source programming language and target architecture (Peyton Jones, Oliva, and Nordin 1997; Peyton Jones, Ramsey, and Reig 1999). Its design accommodates a variety of source languages and leaves room for backend optimization, all without upcalls from the back end to the front end.

C++ is not a universal intermediate language (Conway 1958) or a “write-once, run-anywhere” intermediate language (Lindholm and Yellin 1997). Rather, C++ encapsulates compilation techniques that are well understood, but difficult to implement. Such techniques include instruction selection, register allocation, instruction scheduling, and scalar optimizations of imperative code with loops. Beyond this, C++ also encapsulates the architecture-specific run-time support required for high-level run-time services such as garbage collection, concurrency, debugging, and exception dispatch. It is inappropriate for a back end like C++ to implement such services, so the challenge is to identify low-level, primitive mechanisms that a back end should provide, on top of which a C++ client can implement high-level services.

This paper explains how C++ encapsulates the techniques compilers use to support exception dispatch. It makes the following contributions:

- We present the two mechanisms that C++ uses to specify interprocedural control flow: weak continuations that do not outlive their procedure activations, and call-site annotations (Section 4). These mechanisms support, in a single framework, three well-known ways of implementing exceptions. C++ also supports continuation-passing style, a fourth implementation technique. Every native-code compiler of which we are aware uses one of these four techniques to implement exceptions.

- To define these mechanisms precisely, we present the intermediate language Abstract C++ and its formal operational semantics (Section 5). Abstract C++ is easily derived from C++ source.

- It is not immediately obvious how standard analyses and optimizations should be implemented in the presence of exceptions. We therefore present an algorithm for converting abstract C++ into a dataflow graph (Section 6). This may be a novel way of documenting an intermediate representation, and it should be directly useful to implementors, who can then use standard dataflow analyses to produce accurate and safe code even in the presence of exceptions and exception handlers; exceptions need not be treated as special cases. Therefore, a single optimizer should suffice for all C++ programs, regardless of the original source language.

- Our approach is simple and can be applied to other intermediate forms and it illuminates the design space of exception-dispatch mechanisms.

Not everything here is new. For example, any compiler writer who thinks hard may decide to use extra flow edges to express the optimization constraints of exceptions (Hsieh, Cylbensen, and Hwu 1996). But the literature on optimizing in the presence of exceptions is sparse; Hennessey (1981) and Chase (1994b) are rare exceptions. We do not know of other work that presents language-independent techniques for such optimization.
2. WHAT'S THE PROBLEM?

Since we intend to provide exception mechanisms that suffice to support a variety of programming languages, we begin by surveying the mechanisms that are widely used. We are aware of four techniques that are used to transfer control from the point where a source-language exception is raised to the point at which that exception is handled.

- **Stack cutting** sets the stack pointer and the program counter to point directly to the handler. In a native-code compiler, this technique can be very fast, but it does not restore the values of callee-saves registers; in general, these values may be distributed throughout the stack. This technique may be best suited to implementations that use no callee-saves registers. Objective CAML uses this technique, as do many Common Lisps and pre-Scheme Lisps.

In C code, **setjmp** and **longjmp** cut the stack, but they typically save and restore lots of state: the size of a **jmp_buffer** is 6 pointers on Pentium/Linux, 19 on Sparc/Solaris, and 84 on Alpha/Digital-Unix. Although **setjmp** and **longjmp** do not always use the entire buffer, they are significantly more expensive than a native-code stack cutter, which saves 2 pointers. On the SPARC, **longjmp** pays the additional penalty of flushing register windows. Because of the performance penalty, **setjmp** and **longjmp** are used only by implementations that compile to C, such as SRC Modula-3.

- **Run-time stack unwinding** uses the run-time system to unwind the stack one frame at a time until the handler is reached. The run-time system restores the values of callee-saves registers as it unwinds the stack, typically by interpreting tables deposited by the back end. Operating systems may provide support for run-time stack unwinding; for example, the MIPS ABI provides a "run-time procedure table," and Digital Unix includes a well-specified ABI for call stacks. The native-code back ends of Polytechnic Modula-3 use this technique, as do the Java Virtual Machine and many C++ compilers.

- **Native-code stack unwinding** uses specialized code in each procedure to unwind the stack when a "non-local return" or "exceptional termination" is called for. Because the compiler generates native code to do the unwinding, no interpretive overhead is involved. The Self compiler uses this technique.

- In **continuation-passing style**, the potential exception handlers are represented by an exception continuation. Generated code raises an exception by making a tail call to this continuation. The continuation decides which handler applies. Again, the compiler generates specialized code for each handler. Standard ML of New Jersey uses this technique.

C++ supports continuation-passing style through fully general tail calls (Peyton Jones, Oliva and Nordin 1997), which require no further explanation. We therefore discuss only the first three mechanisms. Although these mechanisms are well understood, supporting any of them requires intimate cooperation among the optimizer, the code generator, and the run-time system.

```c
/* Ordinary recursion */
export sp1;
sp1( bits32 n ) {
  bits32 s, p;
  if n == 1
    return( 1, 1 );
  else {
    s, p = sp1( n-1 );
    return( s+n, p*n );
  }
}

/* Tail recursion */
export sp2;
sp2( bits32 n ) {
  jump sp2_help( n, 1, 1 );
}

sp2_help( bits32 n, bits32 s, bits32 p ) {
  if n==1
    return( s, p );
  else {
    jump sp2_help( n-1, s+n, p*n )
  }
}
```

Figure 1: Three procedures that compute the sum $\sum_{i=1}^{n}$ and product $\prod_{i=1}^{n}$ is written in C--.

- Correct exception dispatch depends both on the semantics of exceptions in the source language and on the representation of the call stack on the target machine; the interactions may be subtle.

- Optimization is fundamentally affected by exceptions. Optimizing compilers may have rather ad hoc modifications that make the optimizers "do the right thing" for the exception semantics of a particular language.

The easy way out is for the code generator to know the language-specific details of exception semantics, and for the run-time system to know the code generator's stack layouts and register-saving protocols. But such intimate cooperation is not available to a reusable code generator, which must support multiple source languages on multiple target architectures.

A main contribution of this paper is to show how a reusable code generator can cooperate with front ends at arm's length, and yet still support a variety of exception semantics in an architecture-independent way. Our key observation is that a language alone cannot provide a sufficiently flexible interface. C++ includes not only a language, which cooperates with the source-language compiler, but also a run-time system, which cooperates with the source-language run-time system.
3. **C--: A PORTABLE ASSEMBLER**

Before discussing how C-- deals with exceptions, we sketch the language and its run-time system; more details can be found in Peyton Jones, Oliva, and Nordin (1997) and Peyton Jones, Ramsey, and Reig (1999). To give a feel for C--, Figure 1 presents three C-- procedures, each of which computes the sum and product of the integers 1..n.

### 3.1 The C-- language

Much of C-- is unremarkable. C-- has parameterized procedures with declared local variables. A procedure body consists of a sequence of statements, which include assignments, conditionals, gotos, calls and jumps (tail calls).

Figure 1 illustrates two features that are common in all machine languages, but less common in some programming languages. First, a procedure may return multiple results. For example, all the procedures in Figure 1 return two results and sp1 contains a call to a multi-result procedure (namely sp itself). Second, a C-- procedure may explicitly tail-call another procedure. For example, sp2 tail-calls sp2 help (using "jump"), and the latter tail-calls itself. A tail call has the same semantics as a regular procedure call followed by a return, but it is guaranteed to deallocate the caller’s resources (notably its activation record) before the call.

C-- has an extremely modest type system: the only types are words and floating-point values of various sizes, e.g., bits8, bits16, bits32, bits64, float32, and float64. For each target architecture, each implementation of C-- designates one of the bits as the “native data-pointer type” and one of the “native code-pointer type.” For example, the name of a procedure like sp1 in Figure 1, denotes an immutable value of the native code-pointer type.

By intent, the C-- type system does not protect the programmer—its sole purpose is to direct the C-- compiler’s use of machine resources; in particular, its mapping of variables to registers. For example, C-- does not check the number or types of arguments passed to a procedure.

Both local and global variables model machine registers, not memory locations. Therefore, variables have no addresses—whenever possible, the C-- back-end maps them to registers. C-- also permits the programmer to declare names that refer to blocks of memory allocated either globally or in a procedure's activation record, but these names do not stand for variables; they stand for addresses of memory blocks, and as such they denote immutable values of the native data-pointer type.

All memory access is explicit. For example, the statement

```
bits32[x] = bits32[y] + 1;
```

loads a 32-bit word from the memory location whose address is in the variable y, increments it, and stores it in the memory location whose address is in the variable x.

### 3.2 Programs as graphs

We regard a C-- program as the textual description of a control-flow graph, or rather, a set of named control-flow graphs, one for each procedure. C-- syntax is designed to make all intraprocedural control-flow edges explicit.

For example, a label, like loop in sp3, names a node in the graph, and a goto creates an edge to the specified label. The target of a goto must be a label in the same procedure. A label is a value, so the target of a goto can be computed at run time, but such a goto must statically identify all possible targets. So the C-- compiler can include those edges in its control-flow graph.

### 3.3 The run-time systems

We assume that an executable program is built by linking together three parts, each of which may be found in object files, libraries or a combination:

- The front end translates the high-level source program into one or more C-- modules, which the C-- compiler translates to generated object code.
- The front end comes with a (probably large) front-end run-time system. This run-time system implements all policy, as well as any mechanisms that depend on the source language. These may include a garbage collector, exception dispatcher, thread scheduler, etc. The front-end run-time system is written in a programming language designed for humans, not in C--; here we assume it is written in C.
- Every C-- implementation comes with a (hopefully small) C-- run-time system. This run-time system encapsulates architecture-specific mechanisms, and it provides services to the front end run-time system through a C-language run-time interface (Peyton Jones and Ramsey 1998). Different front ends may interoperate with the same C-- run-time system.

The main service provided by the C-- run-time interface is to present the state of a suspended C-- computation (“thread”) as a stack of abstract activations. Operations are provided to walk down the stack; to get information from an activation; to make a particular activation become the topmost one; and to change the resumption point of the topmost activation. These operations are summarized in Table 1; the latter three operations are discussed in Section 4. Given knowledge of stack layout, implementing these operations is straightforward; the representation of an activation is likely to include copies of callee-saves registers and a pointer to an activation record on the real call stack.

C-- syntax enables a front end to associate with each call site one or more arbitrary static data blocks, or descriptors, each of which is allocated and initialized by the front end. The syntax is not important in this paper. At run-time, the C-- run-time interface provides GetDescriptor, which returns the n’th descriptor associated with a particular activation.

How is control transferred between a running C-- computation and a run-time system? A front-end runtime may create many C-- threads, each of which makes a coroutine call to the front-end runtime to request services like thread switching or exception dispatch. Or a runtime may create a single C-- thread, which runs on the system stack and requests services by making ordinary calls to the front-end runtime. In either case, the C-- thread initiates the interaction by calling the special C-- procedure yield.
### Table 1: The C++ run-time interface

<table>
<thead>
<tr>
<th>Method</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>Resume(t)</code></td>
<td>Resumes C++ thread t.</td>
</tr>
<tr>
<td><code>FirstActivation(t, &amp;a)</code></td>
<td>Sets a to “currently executing” activation of thread t.</td>
</tr>
<tr>
<td><code>NextActivation(&amp;a)</code></td>
<td>Mutates a to point to the activation to which a will return (normally a’s caller).</td>
</tr>
<tr>
<td><code>SetActivation(t, a)</code></td>
<td>Arranges for thread t to resume execution with activation a.</td>
</tr>
<tr>
<td><code>SetUnwindCont(t, n)</code></td>
<td>Arranges for thread t to resume execution by unwinding to the n’th continuation of the activation with which it is set to resume.</td>
</tr>
<tr>
<td><code>SetCutToCont(t, k)</code></td>
<td>Arranges for thread t to resume execution by cutting the stack to continuation k.</td>
</tr>
<tr>
<td><code>FixContParam(t, n)</code></td>
<td>Returns a pointer to the location in which the n’th parameter of the currently-set continuation will be returned to thread t.</td>
</tr>
<tr>
<td><code>GetDescriptor(a, n)</code></td>
<td>Returns a pointer to the n’th descriptor associated with activation a.</td>
</tr>
</tbody>
</table>

### 4. IMPLEMENTING HIGH-LEVEL EXCEPTIONS IN C++

We now turn our attention to exceptions. Whatever the details, exceptions change the flow of control. The destination of an exceptional control transfer is usually called a handler; finding a handler and transferring to it is called exception dispatch. C++ models handlers as continuations and it provides several control-transfer mechanisms. C++ uses annotations on call sites to tell the optimizer what exceptional control transfers can take place. These annotations also help implement the control-transfer mechanisms. This section describes the mechanisms; Appendix A shows examples of their use.

#### 4.1 C++ Continuations

We model an exception handler as a C++ continuation, which is a bit like a label with parameters.

```cpp
def (bits32 x, bits32 y) {  
    float64 w;  
    ...  
    g(x, k) also cuts to k;  
        /* k may be "cut to" by g, or  
           by something g calls */  
    return;  
    continuation k(x);  
    ... code for k, mentioning x, y, w ...  
}
```

Here, k is a continuation, which is passed to g. A continuation can be declared only inside a procedure. The x in continuation k(x) is not a binding instance; the “formal parameter” of a continuation must be variables of the enclosing procedure, and therefore they need no type declarations. Like every C++ continuation, k denotes a value, which can be used to transfer control to it, as shown in Section 4.2. The value k encapsulates a stack pointer and a program counter.

A continuation value may be passed to procedures or stored in data structures; its type is the native data-pointer type. Once an activation dies, however, its continuations die too. Invoking a dead continuation is an unchecked run-time error, which it is up to the high-level front end to avoid. C++ continuations are therefore less powerful than, say, Scheme continuations, but they can be implemented very efficiently, without stack copying. To avoid unchecked errors, a front end might protect invocations with a run-time check, or it might impose invariants that guarantee no dead continuation can ever be invoked.

### Figure 2: Alternatives for control transfer

The annotation “also cuts to k” at g’s call site indicates that control might flow from the call directly to k. We discuss call-site annotations in Section 4.4.

#### 4.2 Transferring control to a C++ continuation

A C++ procedure can transfer control to a continuation in any of four ways, each of which has a different cost model. The four mechanisms that C++ provides offer compiler writers alternatives for each of two trade-offs.

- Does raising an exception involve walking the stack? Walking the stack makes raising an exception expensive, but can make it cheaper to enter the scope of a handler.
- Should the bulk of the work be done in generated code or in the run-time system? Using generated code results in larger executables, but they may be faster.

Figure 2 shows the four mechanisms that support these alternatives. In the first row are C++ primitives, which result in generated code. In the second row are entry points in the C++ run-time interface (Table 1). We describe of the four design choices in Figure 2 in turn, starting with the top left corner and working counterclockwise.

**Stack cutting (first column of Figure 2)**

Given a continuation value, the C++ primitive “cut to” invokes the continuation, transferring control directly to the continuation without walking the stack.

```cpp
cut to k(arguments);
```

Here, k is a continuation value, such as that passed to g in the example of Section 4.1. The cut to primitive transfers arguments to conventional locations, truncates the stack to k’s activation, and sets the program counter to k’s program.

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1These locations, which are typically registers, are determined by a calling convention that is private to C++.
counter. All of this takes constant time; there is no stack walk.

Instead of using cut to, the C— program may yield to the front-end run-time system. Once the latter finds a continuation value, it can duplicate the effect of cut to by

1. calling SetCutToCont (Table 1), which cuts the stack.
2. calling FindContParam, which identifies the locations in which the continuation expects parameters,
3. storing the actual parameters in those locations, and
4. calling Resume to pass control back to generated code.

Stack cutting takes constant time, but it imposes the cost of recording the continuation value to be cut to. Because C— places few constraints on the identification of target continuations, different front ends can use different implementation techniques. Here are two common choices:

- Have the program keep track of a single “exception continuation,” perhaps in a register. Commonly, when control is transferred to this continuation, it updates the register to point to a new exception continuation.
- Keep a global stack of continuations; choose the topmost one. Alternatively, keep information about which exceptions were handled by the continuation, and choose the first one that applies to the given exception.

Both choices impose a small cost whenever execution enters or leaves the scope of an exception handler, regardless of whether the exception is raised.

The stack-cutting technique also reduces the utility of calleesaves registers. Normally, we could keep y and w in calleesaves registers across the call to g. But the stack-cutting technique cannot restore the values of y and w before entering k. Why not? Because the values of y and w might have been spilled into any activation in the stack between k’s own activation and the place where the exception is raised. Stack cutting therefore imposes a small performance penalty on any call that can cut to a continuation; the callee-saves registers must be considered killed by flow edges from the call to any cut to continuations. This penalty, too, is paid regardless of whether the continuation is used.

Stack unwinding (second column of Figure 2)

A well-known alternative to stack cutting is to walk the stack, one activation at a time, to discover the topmost activation that can handle a given exception. The idea is that it should cost nothing to enter or leave the scope of a handler; in exchange, we are willing to pay more to raise an exception. C— supports this approach as well.

To use the unwinding technique, the C— program may yield to the front-end run-time system, indicating (in some manner that C— neither knows nor cares about) which exception to raise. The front-end runtime then initializes an activation handle with FirstActivation, and walks the stack using NextActivation (Table 1). For each activation it calls GetDescriptor to find the static descriptor deposited for the activation by the front-end compiler. If this descriptor indicates that this activation can handle the exception, the front-end runtime uses SetActivation to arrange that execution will resume at the activation thus identified.

The suspended call site in this activation should look something like this:

\[ g(x) \] also unwinds to k0, k1;

Here, k0 and k1 are continuations, defined in the same procedure as the call to g. The also unwinds to annotations indicate control flow, just like the also cuts to annotation discussed in Section 4.1. In addition, also unwinds to supports the SetUnwindCont run-time interface call (Table 1). The front-end runtime uses SetUnwindCont(i, n) to arrange that when execution is resumed, it will resume at the n'th continuation in the “unwinds to” list of the call site. Finally, it can use FindContParam as before to find where to store parameters to the continuation, and Resume to resume execution. Figure 9 in Appendix A shows the details.

The unwinding technique described so far is somewhat interpretive: the front-end runtime walks the stack looking at descriptor information until it finds a handler. C— also makes it possible to compile this stack walk, by allowing a procedure to return abnormally to its caller: thus:

\[ \text{return } \langle 0/2 \rangle (\text{return values}); \]

This tells C— that the caller has two abnormal return continuations (in addition to the normal return point) and causes a return to the first (index 0) of these two. A normal return to such a call site would be written

\[ \text{return } \langle 2/2 \rangle (\text{return values}); \]

The call site to which such a call returns must specify precisely the correct number of returns to continuations as are specified in the return statement:

\[ g(x) \] also returns to k0, k1;

where k0 and k1 are, as before, continuations declared in the same procedure as the call site. The statement return \langle 0/2 \rangle (p, q) would return to continuation k0, passing p and q from the return site to the parameters of k0. The normal return continuation is always the last, so a normal return in this case would be return \langle 2/2 \rangle. An unannotated return is equivalent to return \langle 0/0 \rangle.}

Because return transfers control only from a procedure to its caller, all procedures must cooperate to get the effect of compiled unwinding. In a language that specifies statically what exceptions a procedure may raise, one might compile each call site with an abnormal-return continuation for each possible exception. Alternatively, one might use a single abnormal-return continuation to dispatch all exceptions. C— supports both styles efficiently, leaving the choice to the implementor of the front end.

How are these abnormal returns implemented? It would be possible simply to return an additional value from each procedure, which the caller could test to see whether the callee had requested stack unwinding or a normal return. Such a test, however, would add an overhead at every call. The overhead can be eliminated by means of a clever code-generation trick. At the call site, the call instruction is followed not by the code to be executed after a normal re-
turn, but by a table of branches to continuations (Atkinson, Liskov, and Scheifler 1978). Figures 3 and 4 show an example, using the SPARC instruction set. Figure 3 shows the instruction sequence at an ordinary call site. The call instruction leaves its address in register %17, and the instruction in the delay slot is executed immediately after the call, before control is transferred to g. The standard return instruction is jmp %17+8, which skips past the instruction in the delay slot and resumes executing the normal continuation.

Figure 4 shows the instruction sequence at the annotated call site g(x) also returns to k0, k1. The callee uses jmp %17+16 for a normal return; it uses jmp %17+8 or jmp %17+12 to return to continuation k0 or k1. This technique has no dynamic overhead in the normal case. Even in the abnormal case, the only dynamic overhead is a branch to a branch, which is much cheaper than branch followed by test and conditional branch. Atkinson, Liskov, and Scheifler (1978) calls this technique the “branch-table method,” noting that because it adds words to every call site, the space overhead may be “considerable.” On other processor architectures, the run-time overhead may also be considerable; the unusual return address used in the normal case may require extra instructions, and it may also confuse branch-prediction hardware. Still, the branch-table method is used in the Self compiler to implement nonlocal returns. It can also support an efficient implementation of the “vectedored returns” used in the Glasgow Haskell Compiler (Peyton Jones 1992, §9.4).

Whether a stack walk is implemented interpretively, in the run-time system, or in native code, using nonlocal return, it can easily restore the values of the callee-saves registers.

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4.3 Primitive operations that can fail
C- expressions represent pure computations on values; they are evaluated without side effects, which occur only as the result of assignments or calls. What, then, are we to do when such computations fail because of a fault detected by the hardware or the operating system? For example, what about a divide instruction that traps when the divisor is zero? For each such operation we provide two variants:

- The fast-but-dangerous variant (%divu, say) generates the shortest possible code sequence (usually one instruction), but its behavior is unspecified if it fails. %divu(x,0) might cause an interrupt, kill the process, or silently give the wrong answer. The exact behavior will vary between architectures. (An alternative would be to guarantee process abortion, but for certain processors this alternative would impose run-time costs that might be annoying in places where the divisor was provably non-zero.)
- The slow-but-solid variant (%diveu) maps failure into a yield. For example, %diveu is indistinguishable from a procedure defined as follows: %diveu( bits32 p, bits32 q ) {
  if q == 0 then { yield( DIVZERO ) }
  return( %diveu( p, q ) )
}

---

2Even the branch-prediction hardware may work without overhead, because under the standard SPARC calling convention, some C procedures return to %17+8 and some to %17+12.
3Private communication from Craig Chambers, January 1999.

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Indeed, NextActivation does so automatically. So the unwinding technique allows callee-save registers to be used at every call site, even if these values might be used in a continuation.
4.4 Informing the optimizer

Exception dispatch changes the flow of control. If the optimizer knows only that control might be transferred unexpectedly to a continuation, it has to make pessimistic assumptions; for example, some Ada compilers require that if any exception handler uses a variable, that variable must always be kept in memory. As another example, C compilers make pessimistic assumptions about local variables in the presence of set jmp and long jmp. The only portable way to guarantee that local variables will have the right values after a longjmp is to declare them volatile, which many C compilers interpret to mean “always keep the values of these variables on the stack.”

C++ supports aggressive optimization by requiring that the front end tell the optimizer explicitly how an exception dispatch might change the flow of control. This information is conveyed through the annotations attached to call sites, some of which we have mentioned already. The following example shows the complete set:

\[ r = g(x) \] also cuts to k1
also unwinds to k2, k3
also returns to k4
also aborts;

In the normal case, the call to \( g(x) \) returns a value, which is placed in \( r \). However, if the call to \( g \) raises an exception, the exception dispatcher may cut the stack by invoking continuation \( k_1 \) (with a loss of callee-saves register), unwind the stack to continuations \( k_2 \) or \( k_3 \), return to alternate continuation \( k_4 \), or abort the execution of the procedure activation containing the call (e.g., by unwinding or cutting the stack past that activation). The names appearing in these annotations, like \( k_1, k_2, k_3, \) and \( k_4 \), are always names of continuations declared in the same procedure as the call site; the annotations may not name variables or expressions.

The “also” annotations add extra flow edges, from the call site to the specified continuations or to the exit node of the procedure (in the case of also aborts). These edges express precisely the constraints that exception handling imposes, but no more. The annotations cannot reasonably be inferred by the C++ compiler on its own; only the front-end compiler knows which calls can flow to which continuations and which calls can abort. If the control flow cannot be determined accurately, the front end, not C++, decides what approximation is useful.

The also cuts to annotation may also be attached to a cut to statement. An unannotated cut to is considered simply to exit the current procedure, but if the cut to could transfer control to a continuation in the same procedure, it must have an also cuts to annotation naming that continuation.

5. OPERATIONAL SEMANTICS OF C++

So far, our treatment of C++ has been informal, as is common in descriptions of exception handling. But without a precise specification it is impossible to say for sure whether a particular optimization changes the behavior of the program. There is also the risk that a front-end compiler and a C++ compiler might disagree about what happens in some obscure circumstances. Accordingly, in this section we describe C++ formally and precisely.

We define Abstract C++, a language that resembles the flowgraph representations used in optimizing compilers, and we give it a formal operational semantics. The operational semantics uses transition rules to specify the permissible behaviors of both the generated code and the run-time system. We also sketch the translation of C++ to Abstract C++ and the implementation of Abstract C++ on hardware.

A program in Abstract C++ is a partial map \( \chi \) from names to procedures. A procedure is a control-flow graph formed using the nodes defined in Table 2. The range of \( \chi \) includes only nodes of the form Entry \( k \), p, or Yield.

The mutable state of the C++ abstract machine has 7 components:

1. the control \( p \), which represents the current node,
2. the local environment \( \rho \), which maps names to values
3. a set \( cs \) containing the variables of \( \rho \) that are stored in callee-saves registers,
4. a unique integer uid, which is used to enforce the restriction against using dead continuations,
5. a memory \( M \), mapping addresses to values,
6. an argument-passing area \( A \), which is a list of values, and
7. a stack \( s \), which is either empty or is a tuple consisting of a continuation bundle, a local environment, a callee-saves variable set, a unique id, and a stack. A continuation bundle encodes the possible outcomes of a procedure call.

We write a state as follows:

\[ p \rho cs uid M A s \]

5.1 Environments, values, and expressions

An environment is a partial function from names to values. We write the empty environment as \( \bot \). To define an environment that is like \( \rho \) except that it maps \( v \) to \( e \), we write \( \rho[v \rightarrow e] \). We generalize this notation to \( \rho[\overline{v} \rightarrow \overline{e}] \) when we have a list of variables \( \overline{v} \) and a list of expressions \( \overline{e} \), both of the same length. To define an environment that is like \( \rho \) except that it is undefined on the variables in set \( s \), we write \( \rho \setminus s \).

\(^5\) The ML community, which has a long-standing tradition of formal definitions and analyses, is an honorable exception.

\(^6\) It is straightforward to generalize to a machine with separate address spaces for instructions and data.
Entry $k$ $p$

The unique entry node of a procedure with continuations $k$ and first node $p$.

Exit $j$ $n$

Normal exit from a procedure, representing a return to continuation $j$ (the call site must have exactly $n$
alternate return continuations tagged with also returns to).

CopyIn $\bar{v}$ $p$

Put results from a call, or parameters to a procedure or continuation, into variables $\bar{v}$, and continue
with $p$.

CopyOut $\bar{e}$ $p$

Make the values of expressions $\bar{e}$ results of a call, or the parameters to a procedure or continuation, and
continue with $p$.

CalleeSaves $cs$ $p$

Make $cs$ the set of variables in callee-saves registers (by spilling or reloading), and continue with $p$.

Assign $i$ $p$

Assign $e$ to $i$, and continue with $p$.

Branch $c$ $p$

Branch to $p_1$ or $p_2$ when $c$ is true or false.

Call $e_f$ $\kappa$

Call procedure $e_f$, returning to one of the nodes in the continuation bundle $\kappa$. A continuation bundle is a
quadruple $(p_r, p_u, p_c, abort)$, where

$p_r$ are the nodes for continuations listed in also returns to, plus the node for normal returns,

$p_u$ are the nodes for continuations listed in also unwinds to,

$p_c$ are the nodes for continuations listed in also cuts to, and

abort is either True (when a call site is annotated with also aborts) or False (otherwise).

Jump $e_f$

Tail call procedure $e_f$. Exits the current procedure.

CutTo $e_c$

Cut the stack to continuation $e_c$. Exits the current procedure.

Yield

Execute a procedure in the run-time system.

---

Table 2: Kinds of nodes in control-flow graph

To enable variables to denote procedures and continuations as well as basic C++ values, we define a value as one of the
following forms:

- $\text{Bits}_v$ $k$
  
The n-bit value $k$

- $\text{Code}_p$
  
  (A pointer to) the node $p$

- $\text{Cont}_p$
  
  A continuation to the node $p$ in the

stack frame with unique id $u$.

Because C++ expressions have no side effects, we can give the
semantics of an expression $e$ simply by giving an evaluation
function $\mathcal{E}[e]$. The exact definition of $\mathcal{E}$ is not relevant
to this paper, so we present an abbreviated definition that
gives the idea. For simplicity, we assume that the names of
local variables are different from the names of procedures,
so for any $\rho$, dom $p \cap \text{dom } \chi = \emptyset$. Under this assumption, $\mathcal{E}[e] \rho M$ might be defined as follows:

- $\mathcal{E}[v] \rho M = \rho(v)$ if $v \in \text{dom } \rho$
- $\mathcal{E}[\text{Code}_v] \rho M = \text{Code}_v(\chi(v))$ if $v \in \text{dom } \chi$
- $\mathcal{E}[\text{Type}_v] \rho M = \text{load}_\text{type}(M, \mathcal{E}[v] \rho M)$
- $\mathcal{E}[e_1 + e_2] \rho M = \text{Bits}_m(n_1 + n_2)$ if $\mathcal{E}[e_1] \rho M = \text{Bits}_m n_1$

Rules like the $+$ rule would apply to the other built-in operators. The load type and store type operations use the native
byte order of the target machine.

5.2 Transitions of the abstract machine

The C++ abstract machine executes a program by entering the initial state

$$\chi[\text{main}] \vdash 0 0 \vdash \text{nil empty}$$

The machine makes transitions until it reaches a state in which no transitions are possible. If, in that state, the control is Exit 0 0 and the stack is empty, we say the program has terminated normally; otherwise it has gone wrong.

A set of transition rules describes the allowable transitions. Each transition rule has the form:

$$\begin{align*}
\langle \text{State } 1 \rangle \\
\Rightarrow \\
\langle \text{State } 2 \rangle
\end{align*}$$

If the machine is in a state matching $\langle \text{State } 1 \rangle$ then it can move in one step to $\langle \text{State } 2 \rangle$ (suitably instantiated). The rest of this section gives the transition rules.

A procedure’s entry node binds the procedure’s continuations into an empty environment. The incoming environment, $\rho$, is discarded. (The values of parameters are bound later by a CopyIn node.)

$$\begin{align*}
\langle \text{Entry } \bar{k} p \rho \rangle \quad &\quad cs \quad uid \quad M \quad A \quad s \\
\Rightarrow &\quad p \quad \text{addConts}(\downarrow, \bar{k}, \text{uid}) \quad \emptyset \quad uid \quad M \quad A \quad s
\end{align*}$$

where each $k$ in $\bar{k}$ is a pair $(v, p)$ and addConts creates bindings as follows:

- $\text{addConts}(\rho, k, \text{uid})$
  
  $= \rho$

  where $k$ is empty

  $= \text{addConts}(\rho[v \mapsto \text{Cont}(p, \text{uid})], \bar{k}, \text{uid})$

  where $\bar{k}$ is $(v, p)$ followed by $\bar{k}'$.

The sequence $\bar{k}$ lists the continuations declared in the procedure body. Each $k \in \bar{k}$ is a $(v, p)$ pair, where $v$ is the name of the continuation and $p$ is the graph node representing it.

Exit pops an activation, returning to the return continuation named in a return. The value-passing area $A$ may hold return values, placed there by a preceding CopyOut node.

$$\begin{align*}
\langle \text{Exit } j n \rho \quad \text{cs} \quad \text{uid} \quad M \quad A \quad s \quad ((\kappa, \rho', \text{cs}', \text{uid}', s')) \\
\Rightarrow &\quad \rho'[j] \quad \rho' \quad \text{cs}' \quad \text{uid}' \quad M \quad A \quad s'\text{ where } \kappa = (\bar{p}_r', \bar{p}_u', \bar{p}_c', \text{abort})\text{, and } |\bar{p}_r'| = n + 1.
\end{align*}$$
The value-passing area $A$ holds arguments and results that are passed among procedures and continuations. The CopyIn and CopyOut nodes transfer values from and to that area. To enable an implementation to reuse registers in $A$, we specify that CopyIn replace $A$ by the empty list, nil. CopyOut may overwrite $A$ whatever its state.

$$\text{CopyIn } \varphi \rho \text{ cs uid } M A \quad \Rightarrow \quad p \varphi[\varphi \mapsto A] \text{ cs uid } M \text{ nil s}$$

$$\text{CopyOut } \varepsilon \rho \text{ cs uid } M A \quad \Rightarrow \quad p \rho \text{ cs uid } M \varepsilon[\varepsilon]\rho M \text{ s}$$

The optimizer may move values into and out of callee-saves registers.

$$\text{CallSaves } \text{cs}' \rho \text{ cs uid } M A \quad \Rightarrow \quad p \rho \text{ cs}' \text{ uid } M A \text{ s}$$

CallSaves nodes are introduced only by compilers; they are not part of the direct translation of any C-- program into Abstract C--. A CallSaves node corresponds to a mix of spills and reloads.

Assignment to a variable changes the local environment; assignment to memory changes memory.

$$\text{Assign } v e p \rho \text{ cs uid } M A \quad \Rightarrow \quad p \rho[\rho \mapsto e] \varepsilon[\varepsilon]\rho M \text{ cs uid } M A \text{ s}$$

$$\text{Assign type[a]} e p \rho \text{ cs uid } M A \quad \Rightarrow \quad p \rho \text{ cs uid } M A \quad \text{ where } M' = \text{store}_{\text{op}}(M, \varepsilon[a], \rho M, \varepsilon[e], \rho M).$$

Conditional branch is straightforward:

$$\text{Branch } c \text{ pt } p f \rho \text{ cs uid } M A \quad \Rightarrow \quad \text{if } \varepsilon[\varepsilon]\rho M \text{ then pt else pf } \rho \text{ cs uid } M A \text{ s}$$

A call pushes a new activation. (Parameters will have already been placed in $A$ by a CopyOut node.) The continuation bundle is saved on the stack because the callee, not the caller, determines what is executed after the call. Each activation record must have a unique uid.

$$\text{Call } e f \kappa \rho \text{ cs uid } M A \quad \Rightarrow \quad p f \rho \text{ \bot } M A \quad (\kappa, \rho, \text{cs}, \text{uid}, \text{s})$$

where $\varepsilon[\varepsilon]\rho M = \text{Code } p f$, and $u$ is a fresh, unique identifier.

Tail calls are simpler; the appropriate continuation bundle is already on the stack:

$$\text{Jump } e f \rho \text{ cs uid } M A \quad \Rightarrow \quad p f \rho \text{ \bot } M A \quad (\kappa, \rho, \text{cs}, \text{uid}, \text{s})$$

where $\varepsilon[\varepsilon]\rho M = \text{Code } p f$, and $u$ is a fresh, unique identifier.

When the machine tries to cut to a continuation belonging to an activation different from the current one ($\text{uid}' \neq \text{uid}$), it removes a frame from the stack and tries again. A real implementation cuts the stack in constant time, but the abstract machine removes the activations one at a time, so it can go wrong if a suspended call does not have an also aborts annotation.

$$\text{CutTo } e c \rho \text{ cs uid } M A \quad (\kappa, \rho', \text{cs}', \text{uid}', \text{s}') \quad \Rightarrow \quad \text{CutTo } e c \rho \text{ cs uid } M A \text{ s'}$$

where $\varepsilon[e] \rho M = \text{Cont}(p f', \text{uid}')$

$$\text{uid}' \neq \text{uid}'$$

$\kappa = (\tilde{p}_f', \tilde{p}_s', \tilde{p}_e', \text{abort})$

$$p' \in \tilde{p}_e'$$

When the machine finds the right activation ($\text{uid}'' = \text{uid}$), it checks that the call site has an also cut to annotation, and it transfers control to the appropriate continuation. The uid check ensures that the machine never invokes a dead continuation; a program that tries to do so goes wrong. As discussed in Section 4.2, cut to does not restore stored values in callee-saves registers; we model this behavior by removing them from the saved environment $p'$. The Yield node models execution in the run-time system. Unlike the other rules, the rules for Yield do not fully specify which transitions take place; that is there are states in which more than one transition is permitted. This underspecification allows the run-time system to implement a variety of different high-level exception semantics while still respecting the single C-- semantics.

The run-time system may unwind the stack if the suspended procedure has an also aborts annotation:

$$\text{Yield } p \rho \text{ cs uid } M A \quad (\kappa, \rho', \text{cs}', \text{uid}'', \text{s}'') \quad \Rightarrow \quad \text{Yield } p \rho \text{ cs uid } M A \text{ s'}$$

where $\kappa = (\tilde{p}_f', \tilde{p}_s', \tilde{p}_e', \text{True})$. The run-time system may change memory, then transfer control to the normal return continuation, or to a continuation listed in an also returns to or also unwinds to annotation. This transition restores callee-saves registers.

$$\text{Yield } p \rho \text{ cs uid } M A \quad (\kappa, \rho', \text{cs}', \text{uid}', \text{s'}) \quad \Rightarrow \quad \text{Yield } p \rho' \text{ cs uid } \text{M'} \text{ A'} \quad \text{s'}$$

where $\kappa = (\tilde{p}_f', \tilde{p}_s', \tilde{p}_e', \text{abort})$, $p' \in \tilde{p}_e'$, and $A'$ is the right length.
5.3 Translating C to Abstract C

Translation of everything except continuations, calls, jumps, and cuts should be obvious. To translate a continuation, create a *CopyIn* node naming the parameters of the continuation, and whose successor is the statement following the continuation. Associate this node with the continuation. To translate a call, create a *CopyOut* node that puts the values of the parameters in the value-passing area. and the successor of which is a *Call* node containing an expression designating the procedure to be called. The *Call* node’s continuation bundle is computed from the also annotations as described above. If the call returns values, the normal continuation should be a *CopyIn* node binding the return values to the variables on the left-hand side of a call. Jumps and cuts are translated similarly, with the simplification that they never return.

5.4 Implementing the abstract machine

The C abstract machine is designed to be very like a real machine, hiding only the details of the registers, the calling convention, and the instruction set. The control register corresponds to the program counter. A local environment register corresponds to parameters and local variables that are stored in registers or in the activation record; the set of names identifies the values that are stored in call-save registers. The *Call* node, which changes cs, is implemented by instructions that move values into or out of call-save registers. Such instructions include spilis, reloads, and register shuffles. The translation of C to Abstract C does not use call-save registers or the *Call* node, but by including *Call* in Abstract C, we enable Abstract C to represent the results of a code improvement that puts values in call-save registers. Of course, such code improvements much take into account control flow along also cuts to edges; such flow destroys values stored in call-save registers.

The value-passing area A is an abstraction representing those registers that are set aside for passing values and results, as well as overflow areas that may be reserved on the stack. This abstraction may have different concrete representations at different call sites. C supports multiple named calling conventions, and each calling convention may dictate a different representation of A. We also intend that C optimizers be free to choose customized value-passing mechanisms when possible, e.g., for passing parameters to procedures all of whose call sites are known.
Finally, the abstract stack corresponds to the machine's stack of activation records. On the stack, a continuation bundle can be represented just by its program counter PC, which in general points to a branch table (Section 4.2). If the run-time system needs to find \( f_u \), it can use PC to look them up in a table.

The wid exists only to ensure that a program which tries to use a dead continuation goes wrong. Because using a dead continuation is an unchecked run-time error, the wid need not be represented an implementation. It may nevertheless be useful to represent it explicitly in order to debug front-end compilers that generate code which goes wrong.

Values of the form \( Bit_b \ n \) are, of course, the basic values of machines. A value of the form \( Code \ p \) is represented by a pointer to the instructions for \( p \). There are several ways to implement a continuation value \( Cont(p, u) \). One possible implementation is to allocate two words in the current activation record and to represent \( Cont(p, u) \) as a pointer to this pair. With a bit more cleverness, it may be possible to allocate only the program counter in the current activation record and use the pointer to that location as the initial stack pointer for the continuation. In either case, some continuation values will need to encapsulate locations in which to store parameters that cannot fit in registers. This information, too, can be made implicit.

6. **OPTIMIZING C-- PROGRAMS**

Table 3 gives rules for adding dataflow information to a C-- procedure, in terms of definitions, uses, copies, and kills. This information is enough to enable standard optimizations like common-subexpression elimination, partial-redundancy elimination, constant propagation, copy propagation, dead-code elimination, code motion, etc. The optimizer can perform all the usual rearrangements, provided it respects the dataflow and it doesn't insert code after \( Exit \), \( Jump \), \( CallTo \), or the abort part of a continuation bundle.

Figure 5 shows an example C-- procedure, and Figure 6 shows its translation into Abstract C-- and the dataflow information attached to the Abstract C--. The dataflow information is expressed as a static single-assignment (SSA) numbering of the variables (Alpern, Wegman, and Zadeck 1988; Rosen, Wegman, and Zadeck 1988; Appel 1998). The elements of the A array may be mapped onto different hardware registers depending on which nodes define and use them and the conventions for passing parameters and return values are. For example, in Figure 6, variables \( A[1] \) and \( A[1][2] \) should be mapped to the register holding the first parameter to a call, but variables \( A[1][3] \), \( A[1][4] \), and \( A[1][5] \) should be mapped to the register holding the first result from a call.

---

7We do not intend that C-- itself be compiled using continuation-passing style, although of course C-- can easily represent high-level programs that are compiled using continuation-passing style. A front end would perform a CPS transformation and build explicit closures to represent continuations. The code in these closures would be represented by C-- procedures, to which control would be transferred using jump. The weak C-- continuations are useful only for nonlocal exits, not for representing first-class continuations.

8The operation kill \( v \) may be defined as assigning a bogus value, e.g. \( v := \bot_{\text{wrong}} \) and any computation that depends on \( \bot_{\text{wrong}} \) may be defined to have gone wrong.

f( bits32 a ) {
  bits32 b, c, d;
  b = a;
  c = a;
  b, c = g( c )
  also unwinds to k;
  c = b + c + a;
  return( c );
}

**Figure 5: Example procedure**

**Figure 6: The example procedure’s translation in Abstract C--, and its dataflow graph in SSA form**
7. RELATED WORK

This paper discusses the low-level implementation of exceptions; Drew and Gough (1994) complements this paper by presenting a taxonomy of high-level designs of exception-handling models.

Chase (1994a) and (1994b) provide helpful, clear explanations of the techniques required to implement both synchronous and asynchronous exceptions. Liskov and Snyder (1979) discusses both the programming methodology in which exceptions should be used and the efficiency of their implementation.

Drew, Gough, and Ledermann (1995) discusses implementation of the stack-unwinding technique, in which there is zero dynamic overhead to enter the scope of an exception handler. Their register allocator is unaware of the control-flow edges from call sites to exception handlers, so any variable that is mentioned in a body and a handler must be allocated on the stack. C-‘s also unwind to annotation should enable optimizers to avoid this performance penalty.

Hennessey (1981) discusses errors that can occur if the optimizer does not know about the additional control-flow edges introduced by exceptional termination. It presents dataflow equations that a front-end compiler can use to compute the annotations it must place at each C- call site. It also discusses interprocedural analyses that compute potential effects on global variables. Such analyses could be accommodated in C- by splitting M. This scheme would require additional annotations for stores, fetches, calls, jumps, and invocations.

It is hard to find out how real optimizing compilers deal with exceptions, but we believe that most either make pessimistic assumptions or implement ad hoc rules that are tightly bound to the particular exception model of the language being compiled. In either case, all the standard optimizations must be reconsidered in light of the particular semantics chosen for exceptions. For example, Schilling (1998) describes some of the optimizations used in C++, particularly emphasizing the optimization of functions that need not participate in exception dispatch. Our approach allows well-developed optimization technology to be applied to a program that uses exceptions. Appel (1992) does something similar in a functional setting—by identifying exception handlers with continuations. Appel’s compiler can simply use the existing body of optimizations known to be supported by continuation-passing style. (This style could be expressed in C- in a manner much like that of the Appendix, except that jump would be used instead of cut to.)

Bruggeman, Waddell, and Dybvig (1996) introduces “oneshot continuations” which are like general Scheme continuations except that they can be invoked at most once. One-shot continuations would certainly suffice to implement C- continuations, but the implementation is part of the Chez Scheme system, and it is not immediately obvious whether the tradeoffs that are good for Scheme would also be good for C-. The results on segmented stacks, at least, appear highly relevant to a concurrent version of C-.

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References


APPENDIX

A. IMPLEMENTING EXCEPTIONS

We believe that C++ can support a rich variety of source-language exception semantics. Demonstrations would provide supporting evidence, but unfortunately any demonstration necessarily fixes both the semantics of the source language and the cost model used in the implementation. To save space, we sketch only two potential exception dispatchers. Both dispatchers implement Modula-3 exceptions, but they use two different cost models.

A.1 Modula-3 exceptions with zero normal-case overhead

Figure 7 shows a fragment from a game-playing program written in Modula-3. Modula-3 uses TRY-EXCEPT-END to show handlers and their scopes. The statement sequences to the right of the arrows (→) are handlers for the exceptions BadMove and NoMoreTiles. If either of these exceptions is raised anywhere between TRY and EXCEPT, control transfers to the appropriate handler. Otherwise, after the assignment to next, control skips directly from EXCEPT to END. After execution of a handler, control also transfers to END.

Using an implementation based on run-time stack unwinding, this code might be translated into the C- procedure shown in Figure 8.

To see how exception dispatch works, let us suppose that getMove terminates normally, but makeMove discovers that the move cannot be made because it goes off the board. makeMove would contain the Modula-3 statement

```plaintext
procedure TryMove() =
begin
  try
    makeMove(getMove(player));
  next := (next + 1) mod numberPlayers;
  except
    | BadMove(why) => player.badmove(why);
    | NoMoreTiles => player.badmove("too few tiles");
  end;
  INC(movesTried);
end TryMove;
```

Figure 7: Example Modula-3 procedure

TryMove() {  
  bits32 s, t;
  t = getMove(player) also unwinds to k1, k2;
  makeMove(t) also unwinds to k1, k2;
  t = bits32[players];  /* load size of array from its descriptor */
  next = (next + 1) mod t;
  finish;
  movesTried = movesTried + 1;
  return;

  continuation k1(s);
  t = bits32[bits32[player]+12];  /* load address of
  t(s);
  geto finish;

  continuation k2:
  t = bits32[bits32[player]+12];  /* load address of
  t("Not enough tiles");
  geto finish;
}

Figure 8: C- implementation of Modula-3 TryMove, using run-time stack unwinding.

RAISE BadMove("off board");

which might be translated into a yield to awakes the front end runtime and request exception-dispatching service. The details of the particular exception would be pushed onto a global "exception stack"

```plaintext
push_exn_info(Exn_BadMove, "off board");
yield( EXCEPTION);
```

The front-end runtime would invoke the exception dispatcher, a simplified version of which appears in Figure 9. The dispatcher would get the exception information, then call FirstActivation(tcb, &a) to get the activation handle for the topmost activation on the stack. Next it would map the activation handle to a statically allocated exception descriptor for TryMove; mechanisms for implementing this mapping are discussed in Peyton Jones and Ramsey (1998). If the exception raised were not handled by any of the handlers in the descriptor, the dispatcher would then call NextActivation(&a) to get the next frame. Eventually it would find the activation for TryMove, whose exception descriptor states that continuation 0 handles the Modula-3 exception BadMove. (For purposes of SetUnwindCont, we number continuations, starting at zero, in the order in which they
A.2 Modula-3 exceptions in constant time

Some compilers use a different implementation trade-off. A small overhead is added to every TRY-EXCEPT-END, but in exchange, exception dispatch is very efficient—typically a few instructions. The high-level language maintains a stack of handlers, perhaps pointed to by a register. Every exception is dispatched to the handler on top of the stack, and that handler contains code to identify the exception and pass it on to the next handler if necessary.

Using this style of implementation, the procedure TryAMove from Figure 7 could be compiled into the C-- code shown in Figure 10. (This example assumes that the machine’s native data-pointer type is bits32.) The code to raise an exception:

```c
RAISE exn (val);
```

would be compiled into this C--:

```c
k = bits32[exn_top]; /* fetch current handler from stack */
exn_top += sizeof(k); /* pop stack */
invoke kt(exn, val); /* invoke the handler */
```

There is no equivalent to the exception dispatcher in Figure 9; instead, the propagation of exceptions is implemented in the handler itself.