## **Z3** Special Interest Group Meeting

Nov 2-4, 2011, MSR Cambridge, UK

	Wednesday	Thursday	Friday
9.00-9.30 Welcome & Breakfast		Welcome & Breakfast	Welcome & Breakfast
9.30	Tony Hoare	Byron Cook	Carsten Fuhs
	The Verified Software Initiative	Symbolic software model checking with Z3	Automated Termination Analysis for Programming Languages via
	Hillel Kugler	Andrey Rybalchenko	Non-Linear SMT and Term Rewriting
	Z3 for Biology	Towards Automatic Synthesis of Software Verification Tools	Symbolic Backward Reachability with Effectively Propositional Logic
10.30-11.00 Break		Break	Break
11.00	The Z3 Team	Nik Sultana	Paul Jackson
	New and future features of Z3	Solving trust issues using Z3	Proving SPARK-Ada verification conditions using SMT solvers
		Alexander Malkis	Margus Veanes
		Automatic verification of software barriers: Z3 vs. MONA vs. BAPA.	Microsoft.Automata library: application to Bek
12.00-13.30	Lunch	Lunch	Lunch
13.30	Adrien Champion	Chantal Keller	Swen Jacobs
	Assumptio and Stuff: using Z3 in a collaborative parallel formal	Certification of SMT proof witnesses in the Coq proof assistant	SMT-based Reactive Synthesis
	verification framework.		
	Marko Kääramees	Tjark Weber	Marek Trtik
	Symbolic analysis of EFSM models for test generation using Z3	Independent Proof Reconstruction for Z3: An Overview	Overapproximating Program Paths using FOL Formula
	Juhan Ernits	Sascha Boehme	Maria Paola Bonacina
	Application of Z3 in Consistency Based Diagnosis of Hybrid Systems	Proof Automation for Isabelle via Z3	Towards an interpolating DPLL(Gamma+T)
	and Beyond		
15.00-15.30 Break		Break	Break
15.30	Malte Schwerhoff	Jasmin Blanchette	Vladimir Klebanov
	Comparing Verification Condition Generation with Symbolic	Sledgehammer with Z3 Makes Isabelle Happy	Theory reasoning in deductive program verification
	Execution	Konstantin Korovin and Christoph Sticksol	Danis Nicola
	Jusing Dohuggors to Understand Failed Varification Attempts	72 for iDrover Eq: officient ground colving for instantiation based	The ESPMC model checker for multi threaded C
	Using Debuggers to Onderstand Paned Vernication Attempts	first-order reasoning	
	Arlen Cox	Philippe Suter	
	Counterexample Generation for Separation Logic	Programming with Z3	
17.00 Close		Close	Close
18.00	Coach to pick up attendees from MSRC		
19.00	Evening Dinner at Restaurant Alimentum	Evening Dinner at Corpus Christi College	Pub crawl