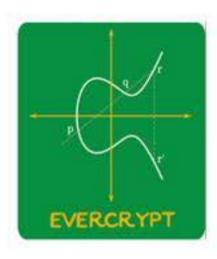
Steel: Scaling up verification in **F***

AYMERIC FROMHERZ, DENIS MERIGOUX

- Efficient memory reasoning is challenging
 - 1. Heap updates with aliasing
 - 2. Invariants on private state
 - 3. Interference among components (e.g. threads)

- Efficient memory reasoning is challenging
 - 1. Heap updates with aliasing
 - 2. Invariants on private state
 - Interference among components (e.g. threads)
- Verified low-level programming is now viable, but requires lots of effort



everparse

- Efficient memory reasoning is challenging
 - 1. Heap updates with aliasing
 - 2. Invariants on private state
 - 3. Interference among components (e.g. threads)
- Verified low-level programming is now viable, but requires lots of effort



- Efficient memory reasoning is challenging
 - 1. Heap updates with aliasing
 - 2. Invariants on private state
 - Interference among components (e.g. threads)
- Verified low-level programming is now viable, but requires lots of effort

F*: Classical Hoare logic and select/update reasoning.
 How to scale?



Type-based Ownership to the Rescue?

Type-based Ownership to the Rescue?

The Rust example:

- Memory safety
- Data-race freedom

By virtue of typing!



Type-based Ownership to the Rescue?

The Rust example:

- Memory safety
- Data-race freedom

By virtue of typing!

What about verification?

- Rust programs aren't proven correct
- Rust programs have unsafe blocks



Steel: A domain-specific language (DSL) shallowly embedded into F*

Targets general-purpose concurrent, systems programming

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic
- Extensible with new constructs, expressed as verified libraries

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic
- Extensible with new constructs, expressed as verified libraries

Main ideas:

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic
- Extensible with new constructs, expressed as verified libraries

Main ideas:

Separated resources and framing for interference control

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic
- Extensible with new constructs, expressed as verified libraries

Main ideas:

- Separated resources and framing for interference control
- 2. Permissions: Exclusive mutable or shared immutable access to resources

Steel: A domain-specific language (DSL) shallowly embedded into F*

- Targets general-purpose concurrent, systems programming
- Always safe, with user-controlled verification
- Core theory proven sound within F*'s logic
- Extensible with new constructs, expressed as verified libraries

Main ideas:

- Separated resources and framing for interference control
- 2. Permissions: Exclusive mutable or shared immutable access to resources
- 3. Fork/join concurrency with locks

- Core memory model
- Resource separation
- Permissions
- Framing
- Concurrency

- Core memory model
- Resource separation
- Permissions
- Framing
- Concurrency
- Case studies: Singly and doubly-linked lists

- Core memory model
- Resource separation
- Permissions
- Framing
- Concurrency
- Case studies: Singly and doubly-linked lists

We can already write Steel programs, but...

- Core memory model
- Resource separation
- Permissions
- Framing
- Concurrency
- Case studies: Singly and doubly-linked lists

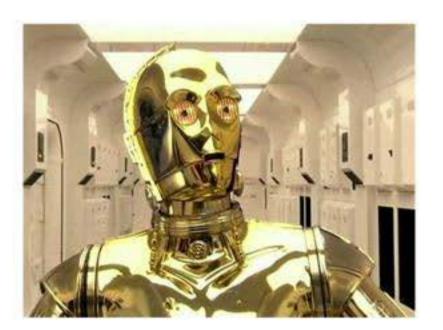
We can already write Steel programs, but...



- Core memory model
- Resource separation
- Permissions
- Framing
- Concurrency
- Case studies: Singly and doubly-linked lists

We can already write Steel programs, but...





1. Footprint

1. Footprint

2. Invariant

1. Footprint

emp: resource

2. Invariant

1. Footprint

emp: resource

2. Invariant

ptr p

arr b

1. Footprint

emp: resource

2. Invariant

ptr p

arr b

$$ptr p \mapsto v$$

arr
$$b \mapsto [> 0;_]$$

1. Footprint

emp: resource

2. Invariant

ptr p

arr b

3. View

 $ptr p \mapsto v$

arr $b \mapsto [> 0;]$

 $R_1 \star R_2$

$$R_1 \star R_2$$

$$(ptr p_1 \mapsto v_1) \star (ptr p_2 \mapsto v_2)$$

$$R_1 \star R_2$$

$$(ptr p_1 \mapsto v_1) \star (ptr p_2 \mapsto v_2)$$

$$slist x \mapsto \ell =$$

$$R_1 \star R_2$$

$$(ptr p_1 \mapsto v_1) \star (ptr p_2 \mapsto v_2)$$

$$slist x \mapsto \ell = match \ \ell \ with \\ | \ [] \rightarrow \\ | \ hd :: tl \rightarrow$$

$$R_1 \star R_2$$

$$(ptr p_1 \mapsto v_1) \star (ptr p_2 \mapsto v_2)$$

slist
$$x \mapsto \ell = \mathsf{match} \ \ell \ \mathsf{with}$$

 $| \ [] \to \mathsf{emp}$
 $| \ \mathsf{hd} :: \mathsf{tl} \to$

$$R_1 \star R_2$$

$$(ptr p_1 \mapsto v_1) \star (ptr p_2 \mapsto v_2)$$

```
slist x \mapsto \ell = \mathsf{match} \ \ell \ \mathsf{with}

| [] \to \mathsf{emp}

| hd :: tl \to (\mathsf{ptr} \ x \mapsto \mathsf{hd}) \star (\mathsf{slist} \ \mathsf{hd}. \, \mathsf{next} \mapsto \mathsf{tl})
```

Resources: typing

Resources: typing

Computations on resources:

RST (α : Type)

```
Computations on resources:

RST (α: Type)

(expects resource)
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
Computations on resources :

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
val alloc : (v: \alpha) \rightarrow RST (pointer \alpha)
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
val alloc : (v: \alpha) \rightarrow RST (pointer \alpha) (expects emp)
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
val alloc : (v: \alpha) \rightarrow RST (pointer \alpha)

(expects emp)

(provides (\lambda p \rightarrow (ptr \ p \mapsto v)))
```

```
Computations on resources:

RST (\alpha: Type)

(expects resource)

(provides (\alpha \rightarrow resource))
```

Allocating a pointer:

```
val alloc : (v: \alpha) \rightarrow RST (pointer \alpha)

(expects emp)

(provides (\lambda p \rightarrow (ptr \ p \mapsto v)))
```

Updating a pointer:

```
Computations on resources:

RST (\alpha: Type)

(expects resource)

(provides (\alpha \rightarrow resource))
```

Allocating a pointer:

```
val alloc : (v: \alpha) \rightarrow RST (pointer \alpha)

(expects emp)

(provides (\lambda p \rightarrow (ptr \ p \mapsto v)))
```

Updating a pointer:

```
val (:=) : (p: pointer \alpha) \rightarrow (v: \alpha) \rightarrow RST unit
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

Allocating a pointer:

(provides $(\lambda_{-} \rightarrow (ptr \ p \mapsto v))$)

(expects (ptr p))

Main idea 1: Resources

1. Footprint

emp: resource

2. Invariant

ptr p

arr b

3. View

 $ptr p \mapsto v$

arr $b \mapsto [> 0; _]$

```
Computations on resources:

RST (\alpha: Type)
(expects resource)
(provides (\alpha \rightarrow resource))
```

Separation-logic style specification:

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) → RST unit
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) →
  RST unit
          (expects (ptr p))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) \rightarrow RST unit (expects (ptr p)) (provides (\lambda_- \rightarrow ptr p))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) \rightarrow RST unit (expects (ptr p)) (provides (\lambda_{-} \rightarrow ptr p)) (ensures (\lambda old_n new \rightarrow ptr p))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) \rightarrow
RST unit

(expects (ptr p))

(provides (\lambda_{-} \rightarrow ptr p))

(ensures (\lambda old_new \rightarrow new (ptr p) = old (ptr p) + 1
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) \rightarrow
RST unit

(expects (ptr p))
(provides (\lambda_{-} \rightarrow ptr p))
(ensures (\lambda old_new \rightarrow new (ptr p) = old (ptr p) + 1
))
```

Separation-logic style specification:

```
val inc : (p: pointer int) \rightarrow (v: ghost int) \rightarrow RST unit (expects (ptr p \mapsto v)) (provides (\lambda_- \rightarrow (ptr p \mapsto v + 1))
```

```
val inc : (p: pointer int) \rightarrow
RST unit

(expects (ptr p))
(provides (\lambda_{-} \rightarrow ptr p))
(ensures (\lambda old_new \rightarrow new (ptr p) = old (ptr p) + 1
))
```

Classic frame rule:

$$\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}$$

Classic frame rule:

$$\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}$$

Classic frame rule:

```
\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}
```

```
val frame :
  (P * Q: resource) →
```

Classic frame rule:

```
\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}
```

```
val frame :
  (P * Q: resource) →
  (P * R: resource) →
```

Classic frame rule:

```
\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}
```

```
val frame : (P \star Q : resource) \rightarrow (P \star R : resource) \rightarrow (f : unit \rightarrow RST \alpha (expects Q) (provides R)) \rightarrow
```

Classic frame rule:

$$\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}$$

```
val frame : (P \star Q : resource) \rightarrow (P \star R : resource) \rightarrow (f : unit \rightarrow RST \alpha \ (expects Q) \ (provides R)) \rightarrow RST \alpha \ (expects \ (P \star Q)) \ (provides \ (P \star R))
```

Challenge: efficient F^* embedding

Challenge: efficient F* embedding

Associative/Commutative rewriting:

Challenge: efficient F* embedding

Associative/Commutative rewriting:

```
P \star Q \star R
```

```
val f: unit \rightarrow RST unit
(expects Q)
(provides Q')
```

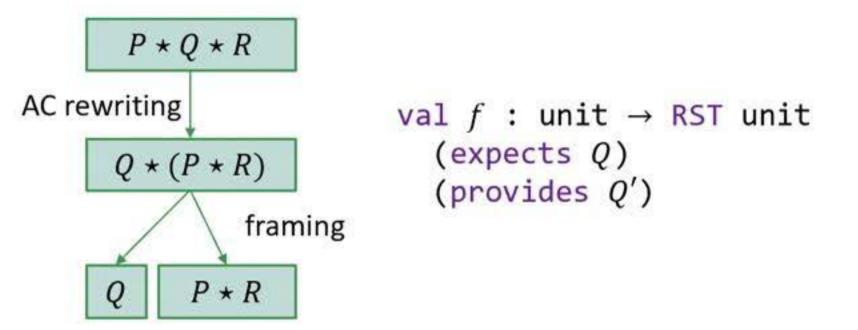
Challenge: efficient F* embedding

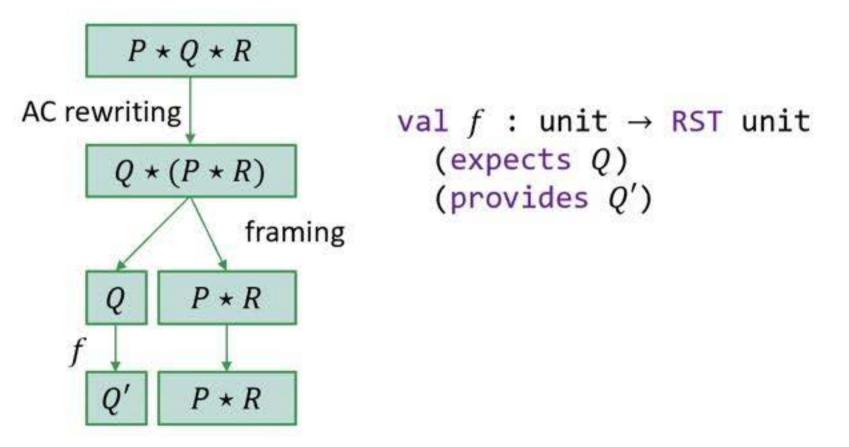
Associative/Commutative rewriting:

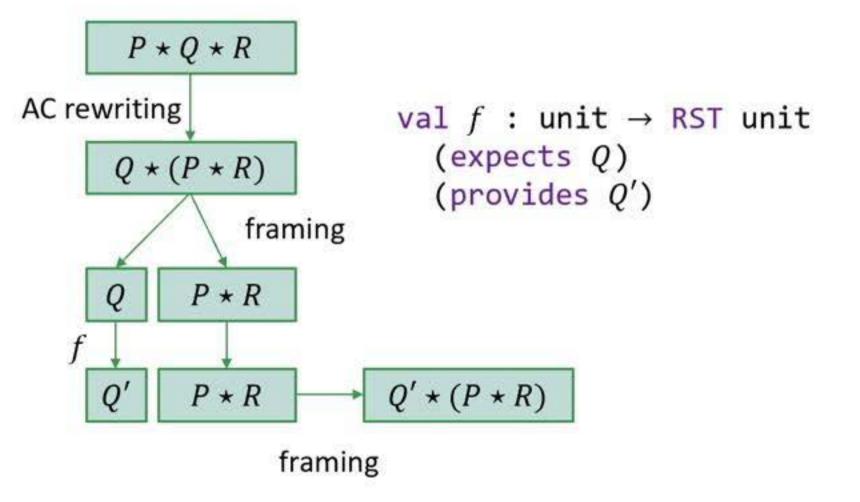
```
P \star Q \star R

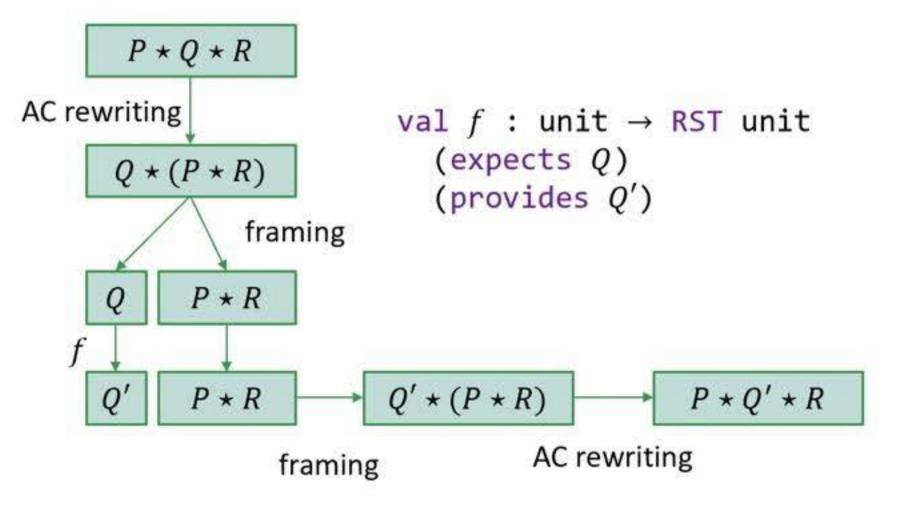
AC rewriting val
Q \star (P \star R)
(example 1)
```

```
val f: unit \rightarrow RST unit
(expects Q)
(provides Q')
```



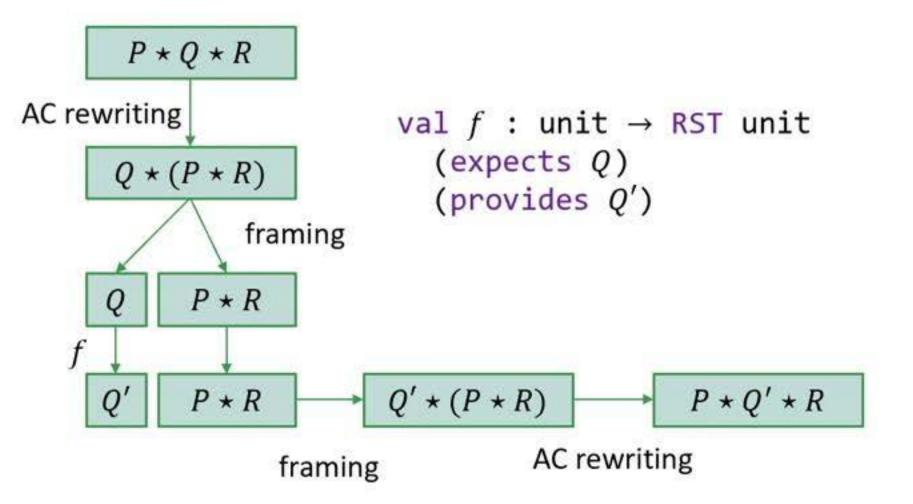




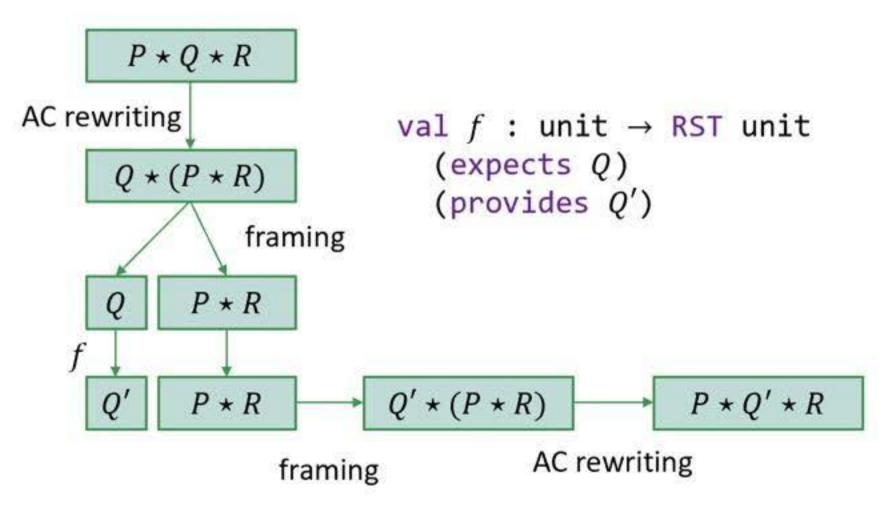


Associative/Commutative rewriting:

Going higher order:



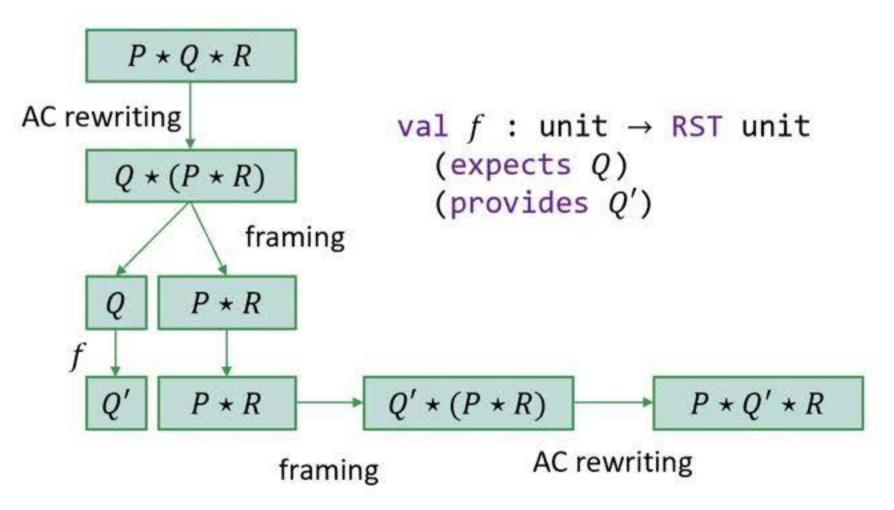
Associative/Commutative rewriting:



Going higher order:

· Current heap model: only first-order logic

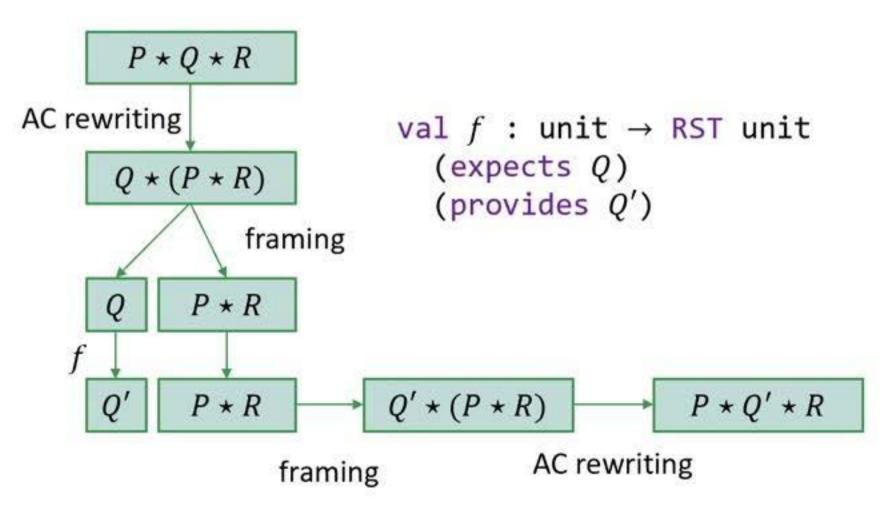
Associative/Commutative rewriting:



Going higher order:

- · Current heap model: only first-order logic
- resource ≈ heap → prop

Associative/Commutative rewriting:

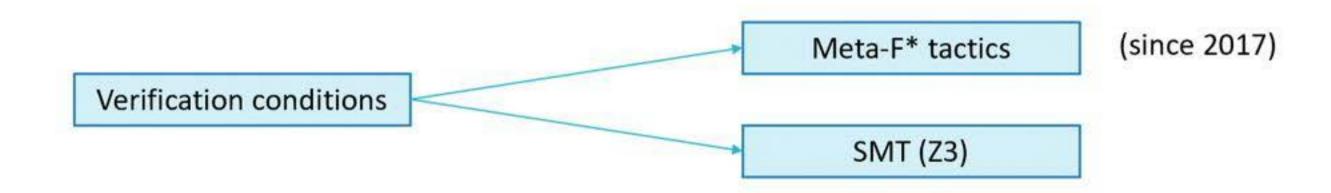


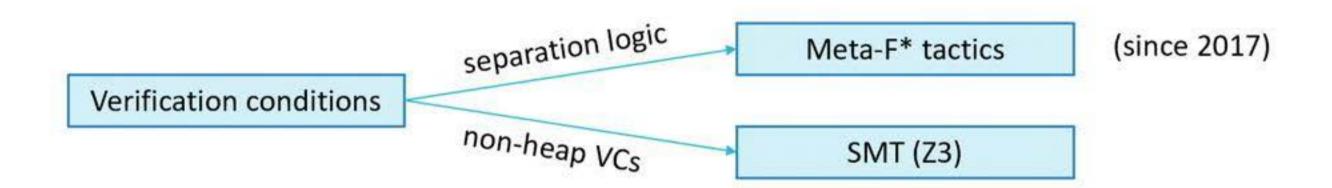
Going higher order:

- Current heap model: only first-order logic
- resource ≈ heap → prop
- *: resource → resource → resource is higher-order

Verification conditions

Verification conditions SMT (Z3)





Updating requires exclusive ownership:

Updating requires exclusive ownership:

```
val (:=) : (p: pointer \alpha) \rightarrow (v: \alpha) \rightarrow RST (pointer \alpha)
(expects (ptr p))
(provides (\lambda p \rightarrow (ptr p \mapsto v)))
```

Updating requires exclusive ownership:

```
val (:=) : (p: pointer \alpha) \rightarrow (v: \alpha) \rightarrow RST (pointer \alpha)
(expects (ptr p))
(provides (\lambda p \rightarrow (ptr p \mapsto v)))
```

What if we only want to read p?

Updating requires exclusive ownership:

```
val (:=) : (p: pointer \alpha) \rightarrow (v: \alpha) \rightarrow RST (pointer \alpha) (expects (ptr p)) (provides (\lambda p \rightarrow (ptr p \mapsto v)))
```

What if we only want to read p?

```
read_only p:
...
p := 0;
```

Read-only resource:

ROR

Read-write resource:

RWR

Read-only resource:

ROR

Read-write resource:

RWR

Read-only resource:

ROR

Read-write resource:

RWR

```
val (!) : (p: pointer \alpha) \rightarrow RST \alpha
```

Read-only resource:

ROR

Read-write resource:

RWR

```
val (!) : (p: pointer \alpha) \rightarrow RST \alpha (expects (RO (ptr p)))
```

Read-only resource:

ROR

Read-write resource:

RWR

```
val (!): (p: pointer \alpha) \rightarrow RST \alpha

(expects (RO (ptr p)))

(provides (\lambda v \rightarrow RO (ptr p \mapsto v)))
```

Read-only resource:

ROR

Read-write resource:

RWR

Pointer dereference:

```
val (!): (p: pointer \alpha) \rightarrow RST \alpha

(expects (RO (ptr p)))

(provides (\lambda v \rightarrow RO (ptr p \mapsto v)))
```

Read-only resource:

ROR

Read-write resource:

RWR

Pointer dereference:

```
val (!): (p: pointer \alpha) \rightarrow RST \alpha

(expects (RO (ptr p)))

(provides (\lambda v \rightarrow RO (ptr p \mapsto v)))
```

```
val share : (p: pointer \alpha) \rightarrow RST (pointer \alpha)
```

Read-only resource:

ROR

Read-write resource:

RWR

Pointer dereference:

```
val (!): (p: pointer \alpha) \rightarrow RST \alpha

(expects (RO (ptr p)))

(provides (\lambda v \rightarrow RO (ptr p \mapsto v)))
```

```
val share : (p: pointer \alpha) \rightarrow RST (pointer \alpha)
(expects (RW (ptr p)))
```

Read-only resource:

ROR

Read-write resource:

RWR

Pointer dereference:

```
val (!): (p: pointer \alpha) \rightarrow RST \alpha

(expects (RO (ptr p)))

(provides (\lambda v \rightarrow RO (ptr p \mapsto v)))
```

```
val share : (p: pointer \alpha) \rightarrow RST (pointer \alpha)

(expects (RW (ptr p)))

(provides (\lambda p' \rightarrow RO (ptr p) \star RO (ptr p')))
```

Permissions are fractions:

RO
$$R = R_{\text{Perm}=f}, \ 0 < f < 1$$

$$RWR = R_{Perm=1}$$

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit
```

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit

(expects ((ptr p)_{Perm=f} * (ptr p')_{Perm=f'})))
```

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit

(expects ((ptr <math>p)_{Perm=f} * (ptr p')_{Perm=f'})))

(provides (\lambda_{-} \rightarrow (ptr p)_{Perm=f+f'})))
```

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit (expects ((ptr \ p)_{Perm=f} \star (ptr \ p')_{Perm=f'}))) (provides (\lambda_- \rightarrow (ptr \ p)_{Perm=f+f'}))) (requires (gatherable p \ p'))
```

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

Gathering back permissions:

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit (expects ((ptr \ p)_{Perm=f} \star (ptr \ p')_{Perm=f'}))) (provides (\lambda_- \rightarrow (ptr \ p)_{Perm=f+f'}))) (requires (gatherable p \ p'))
```

Within TCB and memory model

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit (expects ((ptr \ p)_{Perm=f} \star (ptr \ p')_{Perm=f'}))) (provides (\lambda_{-} \rightarrow (ptr \ p)_{Perm=f+f'}))) (requires (gatherable p \ p'))
```

- Within TCB and memory model
- Statically checked with SMT

Managing fractional permissions

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

Gathering back permissions:

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit (expects ((ptr \ p)_{Perm=f} \star (ptr \ p')_{Perm=f'}))) (provides (\lambda_- \rightarrow (ptr \ p)_{Perm=f+f'}))) (requires (gatherable p \ p'))
```

- Within TCB and memory model
- Statically checked with SMT
- Users can define scoped sharing, etc.

Main idea 3: Concurrency in Steel

Currently in scope:

- Data-race freedom
- Sequential consistency
- Scoped fork-join model (par combinator)
- Mutable memory shared through locks

Managing fractional permissions

Permissions are fractions:

RO
$$R = R_{Perm=f}, 0 < f < 1$$

$$RWR = R_{Perm=1}$$

Gathering back permissions:

```
val gather : (p: pointer \alpha) \rightarrow (p': pointer \alpha) \rightarrow RST unit (expects ((ptr \ p)_{Perm=f} \star (ptr \ p')_{Perm=f'}))) (provides (\lambda_- \rightarrow (ptr \ p)_{Perm=f+f'}))) (requires (gatherable p \ p'))
```

- Within TCB and memory model
- · Statically checked with SMT
- Users can define scoped sharing, etc.

Main idea 3: Concurrency in Steel

Currently in scope:

- Data-race freedom
- Sequential consistency
- Scoped fork-join model (par combinator)
- Mutable memory shared through locks

Concurrent separation logic:

$${P_1} f {Q_1} {P_2} g {Q_2}$$

$$P_1$$

$$\xrightarrow{f}$$

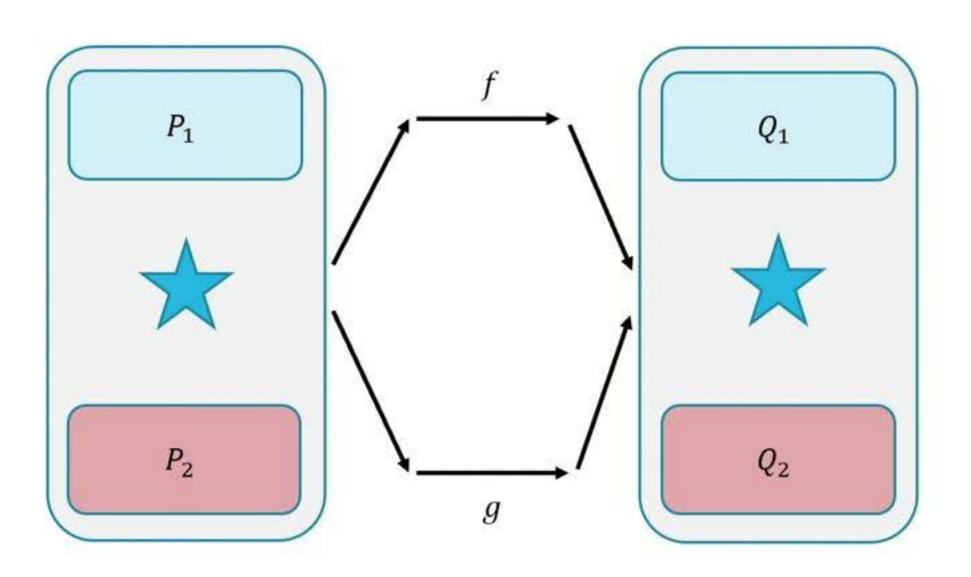
 Q_1

$$P_2$$

$$\xrightarrow{g}$$

 Q_2

$$\frac{\{P_1\}f\{Q_1\} \quad \{P_2\}g\{Q_2\}}{\{P_1 \star P_2\}f \mid\mid g\{Q_1 \star Q_2\}}$$



Concurrent separation logic:

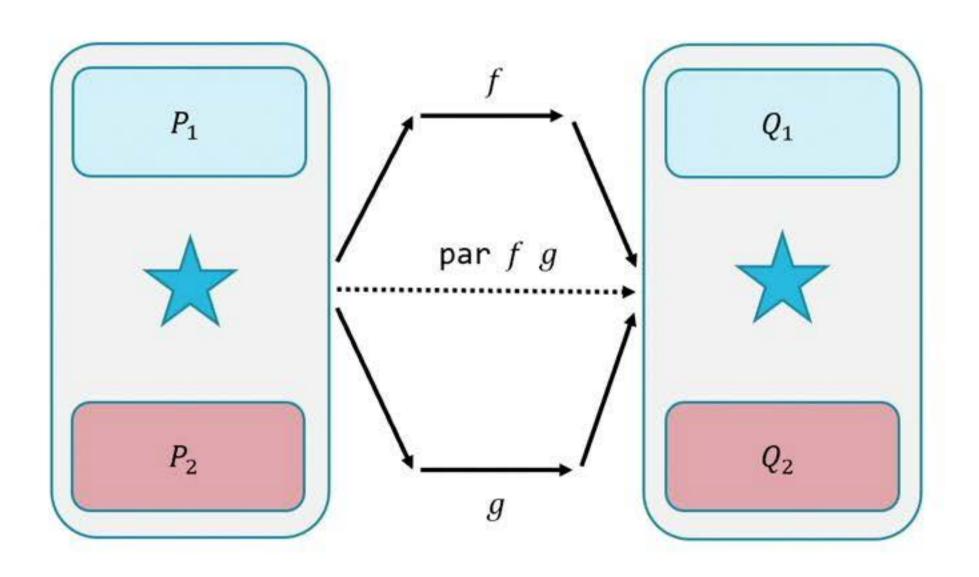
$$\frac{\{P_1\}f\{Q_1\} \qquad \{P_2\}g\{Q_2\}}{\{P_1 \star P_2\}f \mid \mid g\{Q_1 \star Q_2\}}$$

```
val par:

(f: \text{unit} \rightarrow \text{RST } \alpha \ P_1 \ Q_1) \rightarrow (g: \text{unit} \rightarrow \text{RST } \beta \ P_2 \ Q_2) \rightarrow \text{RST } (\alpha \ * \beta)

(\text{expects } P_1 \star P_2)

(\text{provides } Q_1 \star Q_2)
```



```
\{P\} new_lock: lock P {emp}
```

```
\{P\} new_lock: lock P {emp} \{emp\} acquire (\ell: lock P) \{P\} release (\ell: lock P) {emp}
```

```
Stable invariant
```

```
\{P\} new_lock: lock P {emp} {emp} acquire (\ell: lock P) {P} \{P\} release (\ell: lock P) {emp}
```

```
Stable invariant  \{P\} \quad \text{new\_lock: lock } P \quad \{\text{emp}\}   \{\text{emp}\} \quad \text{acquire } (\ell : \text{lock } P) \quad \{P\} \}   \{P\} \quad \text{release } (\ell : \text{lock } P) \quad \{\text{emp}\} \}
```

Concurrent separation logic:

Stable invariant

```
\{P\} new_lock: lock P \{emp\} \{emp\} acquire (\ell: lock P) \{P\} release (\ell: lock P) \{emp\}
```

```
val acquire (ℓ:lock R) →
  RST unit
         (expects emp)
         (provides R)
```

Concurrent separation logic:

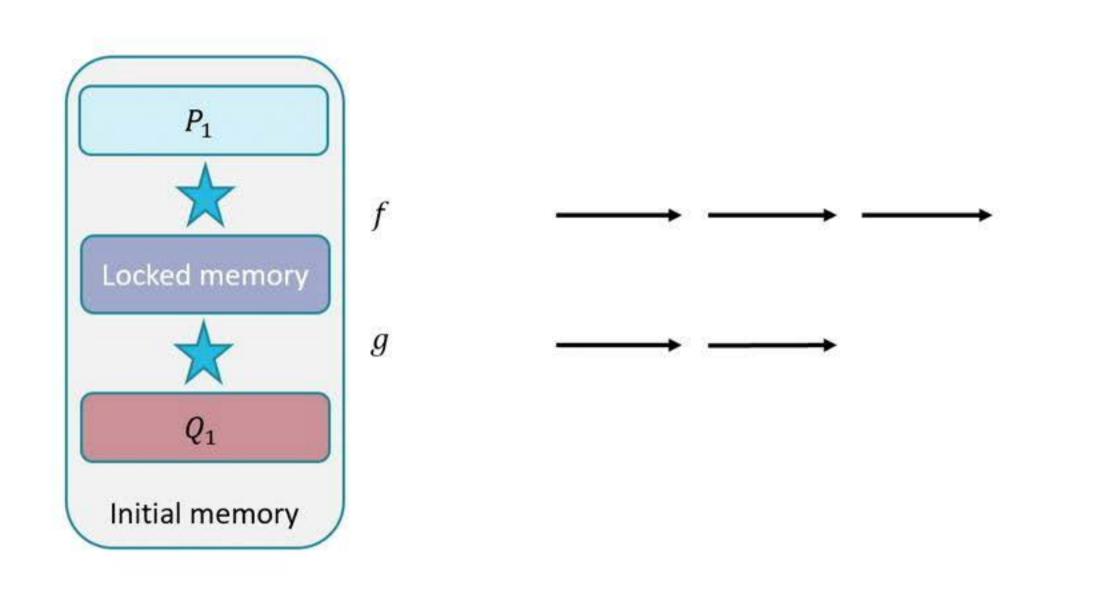
Stable invariant

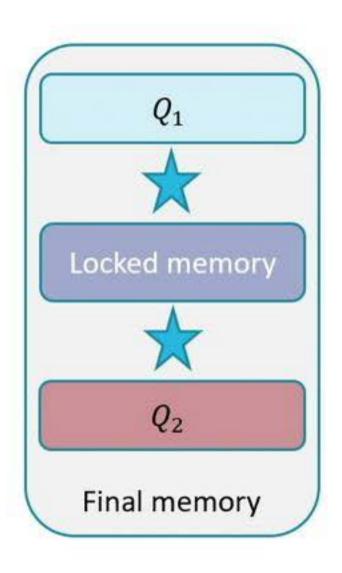
```
\{P\} new_lock: lock P {emp} {emp} acquire (\ell: lock P) \{P\} release (\ell: lock P) {emp}
```

```
val acquire (ℓ:lock R) →
  RST unit
          (expects emp)
          (provides R)
```

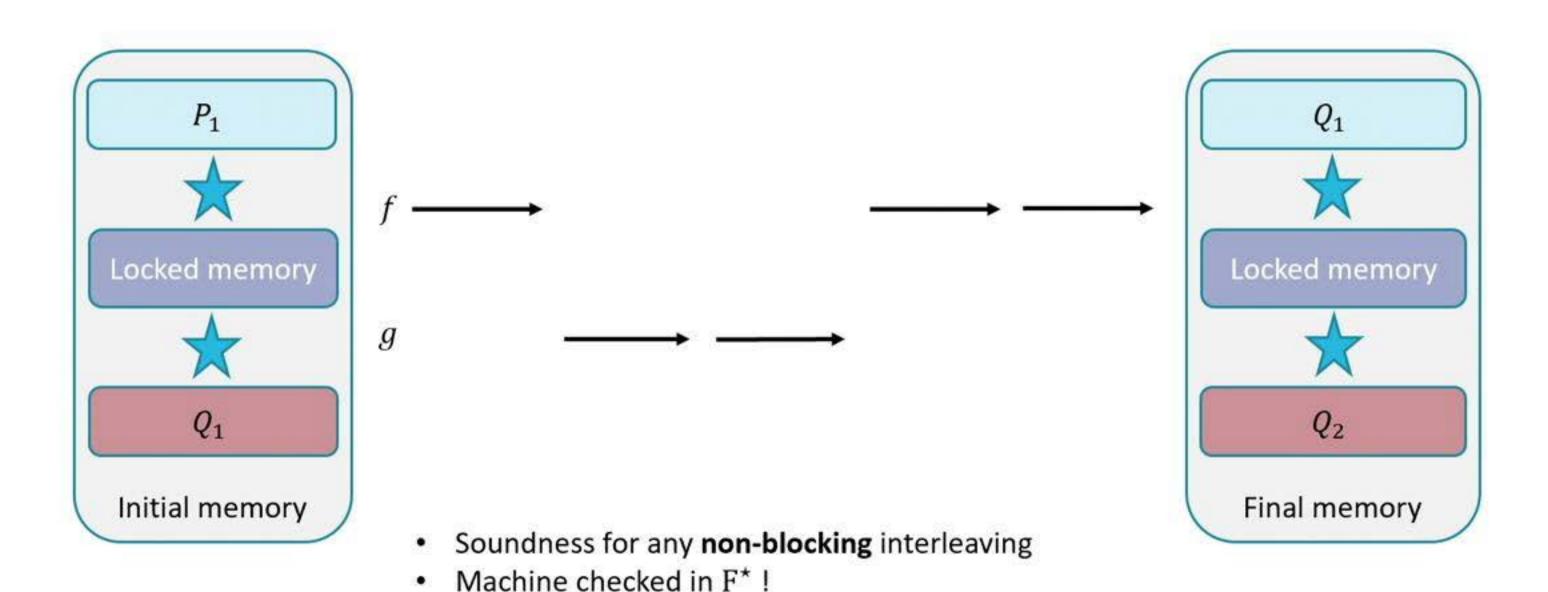
- Lock predicates checked statically
- Lock availability checked at runtime

Soundness of the concurrency model (Work in progress)

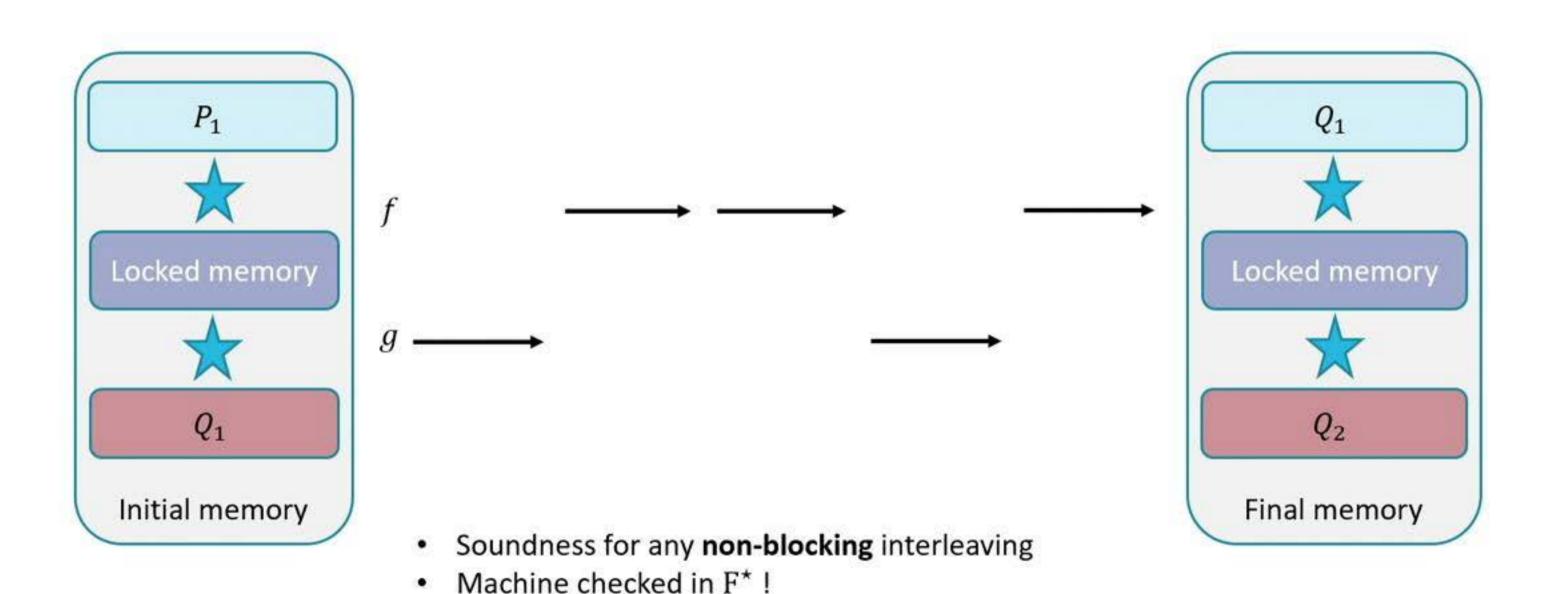




Soundness of the concurrency model (Work in progress)



Soundness of the concurrency model (Work in progress)



```
let rec slist x (\ell:ghost (list cell)) : resource = match \ell with | [] -> emp | hd::tl-> (x \mapsto hd) * (slist hd.next tl)
```

```
let rec slist x (\ell:ghost (list cell)): resource = match \ell with | [] \rightarrow emp | hd::tl-> (x \mapsto hd) * (slist hd.next tl)

val cons (p: pointer) v x \ell : RST unit (expects ((p \mapsto v) * (slist x \ell))) (provides (slist p (v :: \ell)))
```

```
let rec slist x (\ell:ghost (list cell)) : resource = match \ell with | [] \rightarrow emp | hd::tl-> (x \mapsto hd) * (slist hd.next tl) val cons (p: pointer) v x \ell : RST unit (expects ((p \mapsto v) * (slist x \ell))) (provides (slist p (v :: \ell))) val map f p \ell : RST unit (expects (slist p \ell)) (provides (slist p \ell)) (provides (slist p (map_cell f \ell)))
```

```
Steel:
                                                                  Low*:
                                                                  let well_formed x \ell = ... (10 lines)
let rec slist x (\ell:ghost (list cell)) : resource =
                                                                  let footprint x \ell = ... (8 lines)
  match & with
   [] -> emp
                                                                  val cons p \ v \ x \ \ell : ST unit
   hd::tl \rightarrow (x \mapsto hd) \star (slist hd.next tl)
                                                                     (requires
                                                                        well formed x \notin \&\&
val cons (p: pointer) v \times \ell : RST unit
                                                                        disjoint (loc p) (footprint x \ell) &&
  (expects ((p \mapsto v) \star (\text{slist } x \ell)))
                                                                        live p &&
  (provides (slist p(v::\ell))
                                                                        get p == v)
                                                                     (ensures
val map f p \ell : RST unit
                                                                        well_formed p (v :: \ell) &&
  (expects (slist p \ell))
                                                                        modifies (footprint p (v :: \ell)))
  (provides (slist p (map_cell f \ell)))
```

cons, head, tail, map specification

- Steel: 30 LOC
- Low*: 100 LOC

Case study: Linked lists implementation

```
let rec map f \ x \ \ell = ...

frame
 ((x \mapsto \operatorname{hd} \ \ell) \ \star \ \operatorname{slist} \ (\operatorname{hd} \ \ell) .\operatorname{next} \ (\operatorname{tl} \ \ell)) 
 (\lambda \ \_ \to (x \mapsto f \ (\operatorname{hd} \ \ell)) \ \star \ \operatorname{slist} \ (\operatorname{hd} \ \ell) .\operatorname{next} \ (\operatorname{tl} \ \ell)) 
 (\operatorname{update\_cell} \ f \ x); 
frame
 ((x \mapsto f \ (\operatorname{hd} \ \ell)) \ \star \ \operatorname{slist} \ (\operatorname{hd} \ \ell) .\operatorname{next} \ (\operatorname{tl} \ \ell)) 
 (\lambda \ \_ \to (x \mapsto f \ (\operatorname{hd} \ \ell)) \ \star \ \operatorname{slist} \ (\operatorname{hd} \ \ell) .\operatorname{next} \ (\operatorname{map\_cell} \ f \ (\operatorname{tl} \ \ell)) ) 
 (\operatorname{map} \ f \ (\operatorname{hd} \ \ell) .\operatorname{next} \ (\operatorname{tl} \ \ell))
```

Case study: Linked lists implementation

Steel:

let rec map f x ℓ = ... frame $((x \mapsto \text{hd } \ell) \star \text{slist } (\text{hd } \ell).\text{next } (\text{tl } \ell))$ $(\lambda _ \to (x \mapsto f \text{ } (\text{hd } \ell)) \star \text{slist } (\text{hd } \ell).\text{next } (\text{tl } \ell))$ $(\text{update_cell } f x);$ frame $((x \mapsto f \text{ } (\text{hd } \ell)) \star \text{slist } (\text{hd } \ell).\text{next } (\text{tl } \ell))$ $(\lambda _ \to (x \mapsto f \text{ } (\text{hd } \ell)) \star \text{slist } (\text{hd } \ell).\text{next } (\text{map_cell } f \text{ } (\text{tl } \ell)))$ $(\text{map } f \text{ } (\text{hd } \ell).\text{next } (\text{tl } \ell))$

Ideally:

```
let rec map f x \ell =
...
update_cell f x;
map f (hd \ell).next (tl \ell)
```

Work in progress: Better frame inference

Case study: Doubly-linked lists

- Doubly-linked lists in Steel: 400 LoCs
- In Low*: 4000 LoCs!

Case study: Doubly-linked lists

- Doubly-linked lists in Steel: 400 LoCs
- In Low*: 4000 LoCs!

- Doubly-linked lists are not expressible in Rust without unsafe blocks due to aliasing restrictions
- Steel is expressive enough to capture complex aliasing patterns

Future work

- Improve usability of the framework (3 6 months)
 - Frontend syntax
 - More fine-tuning of SMT queries
 - Additional libraries
 - Complete interoperation with Low*

Future work

- Improve usability of the framework (3 6 months)
 - Frontend syntax
 - More fine-tuning of SMT queries
 - Additional libraries
 - Complete interoperation with Low*
- Concurrency (1 2 months)
 - Deadlock prevention
 - Complete proof of soundness

- Separation logic in F*: Why so long?
 - Separation logic with SMT only is impossible
 - Meta-F*: Tactics + SMT make it possible

- Separation logic in F*: Why so long?
 - Separation logic with SMT only is impossible
 - Meta-F*: Tactics + SMT make it possible
- This summer: The right abstractions with resource typing to make it scale

- Separation logic in F*: Why so long?
 - Separation logic with SMT only is impossible
 - Meta-F*: Tactics + SMT make it possible
- This summer: The right abstractions with resource typing to make it scale
- Many applications targeted: Beyond crypto verification
 - Concurrent networking protocols, e.g. Quic
 - Critical systems components in Azure: Parts of Hyper-V? Azure CCF?

- Separation logic in F*: Why so long?
 - Separation logic with SMT only is impossible
 - Meta-F*: Tactics + SMT make it possible
- This summer: The right abstractions with resource typing to make it scale
- Many applications targeted: Beyond crypto verification
 - Concurrent networking protocols, e.g. Quic
 - Critical systems components in Azure: Parts of Hyper-V? Azure CCF?
- Many possible synergies:
 - Verifying Rust programs inside of Steel?
 - · Verifying Verona components (ownership-based systems language from MSR Cambridge)?

- Separation logic in F*: Why so long?
 - Separation logic with SMT only is impossible
 - Meta-F*: Tactics + SMT make it possible
- This summer: The right abstractions with resource typing to make it scale
- Many applications targeted: Beyond crypto verification
 - Concurrent networking protocols, e.g. Quic
 - Critical systems components in Azure: Parts of Hyper-V? Azure CCF?
- Many possible synergies:
 - Verifying Rust programs inside of Steel?
 - Verifying Verona components (ownership-based systems language from MSR Cambridge)?

fromherz@cmu.edu

denis.merigoux@inria.fr

A frame rule for Steel

Classic frame rule:

$$\frac{\{Q\} f \{R\}}{\{P \star Q\} f \{P \star R\}}$$

In Steel:

```
val frame :  (P \star Q \colon \mathsf{resource}) \to \\ (P \star R \colon \mathsf{resource}) \to \\ (f \colon \mathsf{unit} \to \mathsf{RST} \; \alpha \; (\mathsf{expects} \; Q) \; (\mathsf{provides} \; R)) \to \\ \mathsf{RST} \; \alpha \\ (\mathsf{expects} \; (P \star Q)) \\ (\mathsf{provides} \; (P \star R))
```